

Model Name: GA-Q67M-D2H-B3

SHEET

TITLE

01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	CPU_LGA1155-A
05	CPU_LGA1155-B
06	CPU_LGA1155-C
07	DDR III CHANNEL A
08	DDR III CHANNEL B
09	PCI EXPRESS*16 SLOT
10	PCH_FDI,DMI,USB,PCIE
11	PCH_DP,CLK BUFFER
12	PCH_HOST,SATA,PCI
13	PCH_GPIO,CTRL,AUDIO
14	PCH_PWR,GND
15	PCH_HDMI,DVI,DP
16	PCI EXPRESS*4 SLOT
17	PCI SLOT
18	INTEL LAN
19	ITE 8728 LPC IO
20	COM,LPT
21	BIOS, TPM
22	VCORE /VAXG PWM_ISL6364A-1
23	VCORE /VAXG PWM_ISL6364A-2
24	DISCRETE POWER I
25	DISCRETE POWER II
26	CPU_VTT ,VCC_SA POWER
27	ATX,RUSB,PROCHOT-

SHEET

TITLE

28	ALC889
29	REAR AUDIO JACK
30	FP,FUSB
31	HWM,KB/MS, FAN CTRL
32	80 PORT,USB UV PROTECT,OV IC
33	
34	
35	
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Title			
Cover Sheet			
Size	Document Number	GA-Q67M-D2H-B3	Rev
Custom			1.0
Date:	Friday, April 15, 2011	Sheet	1 of 32

Model Name: GA-Q67M-D2H-B3

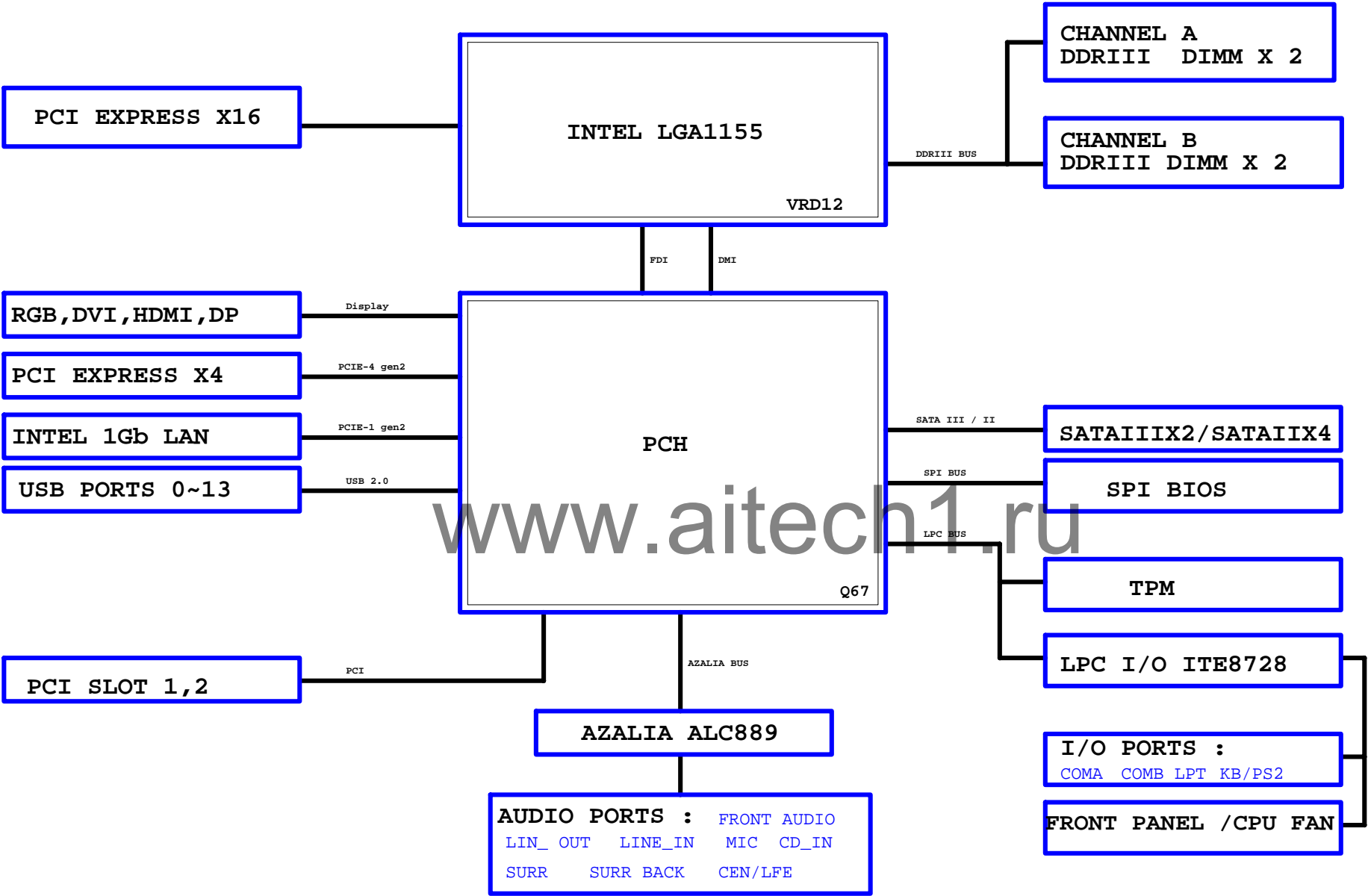
Component value change history

[illegible]

Circuit or PCB layout change

[illegible]

BLOCK DIAGRAM



CPUA

MAAA0	AV27	SA_MA[0]	SA_DS[0]	AK3	DQSA0
MAAA1	AY24	SA_MA[1]	SA_DS[0]	AK2	-DQSA0
MAAA2	AW24	SA_MA[2]			
MAAA3	AV23	SA_MA[3]			
MAAA4	AV23	SA_MA[4]	SA_DS[0]	AJ3	MDA0
MAAA5	AT24	SA_MA[5]	SA_DS[1]	AJ4	MDA1
MAAA6	AT23	SA_MA[6]	SA_DS[2]	AL3	MDA2
MAAA7	AU22	SA_MA[7]	SA_DS[3]	AL4	MDA3
MAAA8	AV22	SA_MA[8]	AJ2	MDA4	
MAAA9	AT22	SA_MA[9]	SA_DS[4]	AJ1	MDA5
MAAA10	AV28	SA_MA[10]	SA_DS[5]	AL2	MDA6
MAAA11	AU21	SA_MA[11]	SA_DS[6]	AL1	MDA7
MAAA12	AT21	SA_MA[12]			
MAAA13	AW32	SA_MA[13]	SA_DS[11]	AP3	DQSA1
MAAA14	AU20	SA_MA[14]	SA_DS[11]	AP2	-DQSA1
MAAA15	AT20	SA_MA[15]			
(7) -SWEA	-SCASA	SA_WE#	SA_DS[8]	AN1	MDA8
(7) -SCASA	-SRASA	SA_CAS#	SA_DS[9]	AN4	MDA9
(7) -SRASA		SA_RAS#	SA_DS[10]	AR3	MDA10
(7) SBAA0	SBAA0	SA_BS[0]	SA_DS[11]	AR4	MDA12
(7) SBAA1	SBAA1	SA_BS[1]	SA_DS[12]	AN2	MDA11
(7) SBAA2	SBAA2	SA_BS[2]	SA_DS[13]	AN3	MDA13
			SA_DS[14]	AR2	MDA14
			SA_DS[15]	AR1	MDA15
(7) -CSA0	-CSA0	SA_CS#	SA_DS[2]	AW4	DQSA2
(7) -CSA1	-CSA1	SA_CS#	SA_DS[2]	AW4	-DQSA2
(7) -CSA2	-CSA2	SA_CS#			
(7) -CSA3	-CSA3	SA_CS#			
(7) CKEA0	CKEA0	SA_CKE[0]	SA_DS[16]	AV2	MDA16
(7) CKEA1	CKEA1	SA_CKE[1]	SA_DS[17]	AW3	MDA17
(7) CKEA2	CKEA2	SA_CKE[2]	SA_DS[18]	AV5	MDA18
(7) CKEA3	CKEA3	SA_CKE[3]	SA_DS[19]	AW5	MDA19
			SA_DS[20]	AU2	MDA20
			SA_DS[21]	AU3	MDA21
			SA_DS[22]	AU5	MDA22
			SA_DS[23]	AY5	MDA23
			SA_DS[3]	AV8	DQSA3
			SA_DS[3]	AW8	-DQSA3
(7) DCLKA0	DCLKA0	SA_CK[0]	SA_DS[24]	AY7	MDA24
(7) -DCLKA0	-DCLKA0	SA_CK#	SA_DS[25]	AU7	MDA25
(7) DCLKA1	DCLKA1	SA_CK[1]	SA_DS[26]	AV9	MDA26
(7) -DCLKA1	-DCLKA1	SA_CK#	SA_DS[27]	AU9	MDA27
(7) DCLKA2	DCLKA2	SA_CK[2]	SA_DS[28]	AV7	MDA28
(7) -DCLKA2	-DCLKA2	SA_CK#	SA_DS[29]	AW7	MDA29
(7) DCLKA3	DCLKA3	SA_CK[3]	SA_DS[30]	AW9	MDA30
(7) -DCLKA3	-DCLKA3	SA_CK#	SA_DS[31]	AY9	MDA31
(7,8) -DDR3_RST		SM_DRAMRST#			
			SA_DS[4]	AV37	DQSA4
			SA_DS[4]	AV36	-DQSA4
			SA_DS[32]	AJ35	MDA32
			SA_DS[33]	AW37	MDA33
			SA_DS[34]	AJ38	MDA34
			SA_DS[35]	AJ36	MDA35
			SA_DS[36]	AW35	MDA36
			SA_DS[37]	AY36	MDA37
			SA_DS[38]	AJ38	MDA38
			SA_DS[39]	AJ37	MDA39
			SA_DS[5]	AP38	DQSA5
			SA_DS[5]	AP39	-DQSA5
			SA_DS[40]	AR40	MDA40
			SA_DS[41]	AR37	MDA41
			SA_DS[42]	AN38	MDA42
			SA_DS[43]	AN37	MDA43
			SA_DS[44]	AR39	MDA44
			SA_DS[45]	AR38	MDA45
			SA_DS[46]	AN38	MDA46
			SA_DS[47]	AN40	MDA47
			SA_DS[6]	AK38	DQSA6
			SA_DS[6]	AK39	-DQSA6
			SA_DS[48]	AL40	MDA48
			SA_DS[49]	AL37	MDA49
			SA_DS[50]	AJ38	MDA50
			SA_DS[51]	AJ37	MDA51
			SA_DS[52]	AL39	MDA52
			SA_DS[53]	AL38	MDA53
			SA_DS[54]	AJ39	MDA54
			SA_DS[55]	AJ40	MDA55
			SA_DS[7]	AF38	DQSA7
			SA_DS[7]	AF39	-DQSA7
			SA_DS[56]	AG40	MDA56
			SA_DS[57]	AG37	MDA57
			SA_DS[58]	AE38	MDA58
			SA_DS[59]	AE37	MDA59
			SA_DS[60]	AG39	MDA60
			SA_DS[61]	AG38	MDA61
			SA_DS[62]	AE39	MDA62
			SA_DS[63]	AE40	MDA63

DDR_0

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LGA1155[F01SC1-F01155-01R]

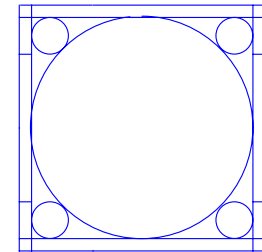
CPUB

MAAB0	AK24	SB_MA[0]	SB_DS[0]	AH7	DQSB0
MAAB1	AM20	SB_MA[1]	SB_DS[0]	AH6	-DQSB0
MAAB2	AM19	SB_MA[2]			
MAAB3	AK18	SB_MA[3]			
MAAB4	AP19	SB_MA[4]	SB_DS[0]	AG7	MDB0
MAAB5	AP18	SB_MA[5]	SB_DS[1]	AG8	MDB1
MAAB6	AM18	SB_MA[6]	SB_DS[2]	AJ9	MDB2
MAAB7	AL18	SB_MA[7]	SB_DS[3]	AJ8	MDB3
MAAB8	AY17	SB_MA[8]	SB_DS[4]	AG6	MDB4
MAAB9	AN18	SB_MA[9]	SB_DS[5]	AJ6	MDB5
MAAB10	AN23	SB_MA[10]	SB_DS[6]	AJ7	MDB7
MAAB11	AU17	SB_MA[11]			
MAAB12	AT18	SB_MA[12]	SB_DS[11]	AM8	DQSB1
MAAB13	AR26	SB_MA[13]	SB_DS[11]	AL8	-DQSB1
MAAB14	AY16	SB_MA[14]			
MAAB15	AV16	SB_MA[15]			
(8) -SWEB	-SCASB	SB_WE#	SB_DS[8]	AM7	MDB8
(8) -SCASB	-SRASB	SB_CAS#	SB_DS[9]	AM10	MDB10
(8) -SRASB		SB_RAS#	SB_DS[10]	AL10	MDB12
(8) SBAB0	SBAB0	SB_BS[0]	SB_DS[11]	AL6	MDB13
(8) SBAB1	SBAB1	SB_BS[1]	SB_DS[12]	AM6	MDB14
(8) SBAB2	SBAB2	SB_BS[2]	SB_DS[13]	AL9	MDB15
(8) -CSB0	-CSB1	SB_CS#	SB_DS[2]	AR8	DQSB2
(8) -CSB1	-CSB2	SB_CS#	SB_DS[2]	AP8	-DQSB2
(8) -CSB2	-CSB3	SB_CS#			
(8) -CSB3					
(8) CKEB0	CKEB0	SB_CKE[0]	SB_DS[16]	AP7	MDB16
(8) CKEB1	CKEB1	SB_CKE[1]	SB_DS[17]	AR7	MDB17
(8) CKEB2	CKEB2	SB_CKE[2]	SB_DS[18]	AP10	MDB18
(8) CKEB3	CKEB3	SB_CKE[3]	SB_DS[19]	AR10	MDB19
			SB_DS[20]	AP6	MDB20
			SB_DS[21]	AR6	MDB21
			SB_DS[22]	AP9	MDB22
			SB_DS[23]	AR9	MDB23
			SB_DS[3]	AN13	DQSB3
			SB_DS[3]	AN12	-DQSB3
(8) DCLKB0	DCLKB0	SB_CK[0]	SB_DS[24]	AM12	MDB24
(8) -DCLKB0	-DCLKB0	SB_CK#	SB_DS[25]	AM13	MDB25
(8) DCLKB1	DCLKB1	SB_CK[1]	SB_DS[26]	AP13	MDB26
(8) -DCLKB1	-DCLKB1	SB_CK#	SB_DS[27]	AP13	MDB27
(8) DCLKB2	DCLKB2	SB_CK[2]	SB_DS[28]	AL12	MDB28
(8) -DCLKB2	-DCLKB2	SB_CK#	SB_DS[29]	AL13	MDB29
(8) DCLKB3	DCLKB3	SB_CK[3]	SB_DS[30]	AR12	MDB30
(8) -DCLKB3	-DCLKB3	SB_CK#	SB_DS[31]	AP12	MDB31
			SB_DS[4]	AN29	DQSB4
			SB_DS[4]	AN28	-DQSB4
			SB_DS[32]	AR28	MDB32
			SB_DS[33]	AP23	MDB33
			SB_DS[34]	AL28	MDB34
			SB_DS[35]	AL29	MDB35
			SB_DS[36]	AP28	MDB36
			SB_DS[37]	AP29	MDB37
			SB_DS[38]	AM28	MDB38
			SB_DS[39]	AM29	MDB39
			SB_DS[5]	AP33	DQSB5
			SB_DS[5]	AR33	-DQSB5
			SB_DS[40]	AP32	MDB40
			SB_DS[41]	AP31	MDB41
			SB_DS[42]	AP35	MDB42
			SB_DS[43]	AP34	MDB43
			SB_DS[44]	AR32	MDB44
			SB_DS[45]	AR31	MDB45
			SB_DS[46]	AR35	MDB46
			SB_DS[47]	AR34	MDB47
			SB_DS[6]	AL33	DQSB6
			SB_DS[6]	AM33	-DQSB6
			SB_DS[48]	AM32	MDB48
			SB_DS[49]	AM31	MDB49
			SB_DS[50]	AL35	MDB50
			SB_DS[51]	AL32	MDB51
			SB_DS[52]	AM34	MDB52
			SB_DS[53]	AL31	MDB53
			SB_DS[54]	AM35	MDB54
			SB_DS[55]	AL34	MDB55
			SB_DS[7]	AG35	DQSB7
			SB_DS[7]	AG34	-DQSB7
			SB_DS[56]	AH35	MDB56
			SB_DS[57]	AH34	MDB57
			SB_DS[58]	AE34	MDB58
			SB_DS[59]	AE35	MDB59
			SB_DS[60]	AJ35	MDB60
			SB_DS[61]	AJ34	MDB61
			SB_DS[62]	AF33	MDB62
			SB_DS[63]	AF35	MDB63

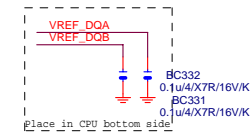
DDR_1

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LGA1155[F01SC1-F01155-01R]

CPU
ILM_BP/1156/CSP

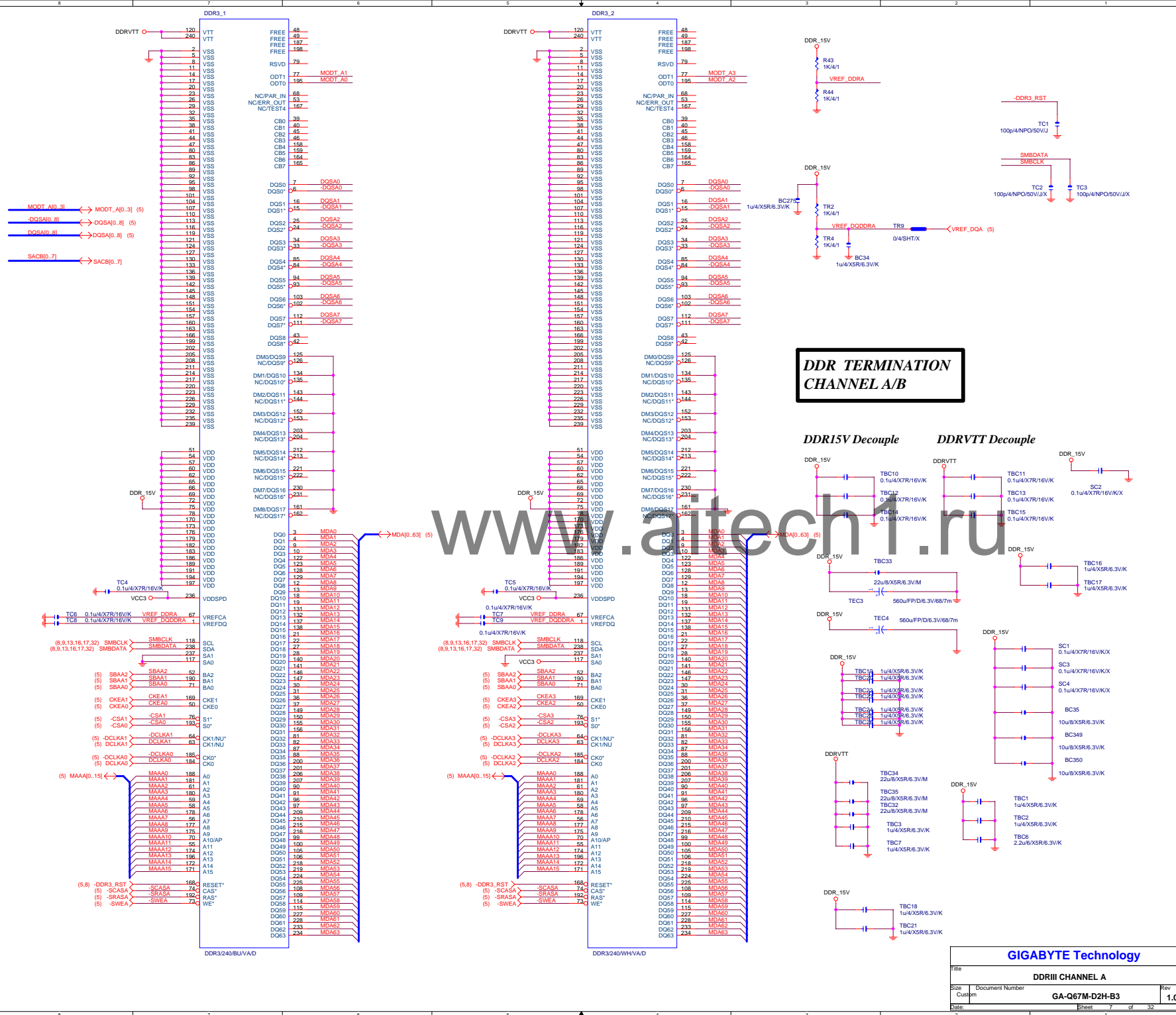
Need check the new CPU ME

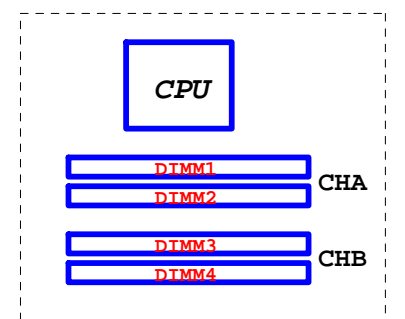


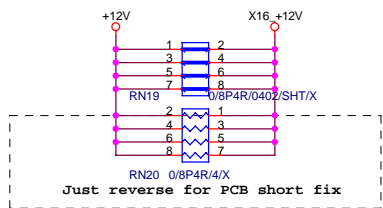
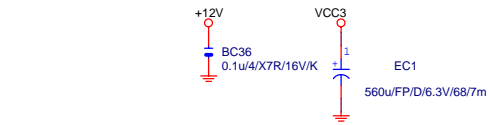
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CPU LGA1156-B

Title	Document Number	Rev
Size	Custpm	1.0
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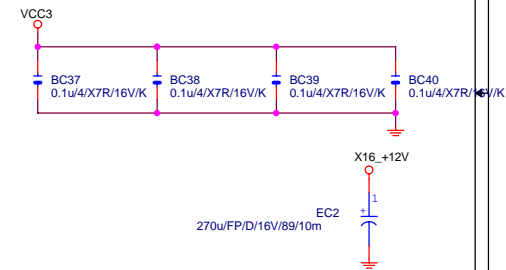
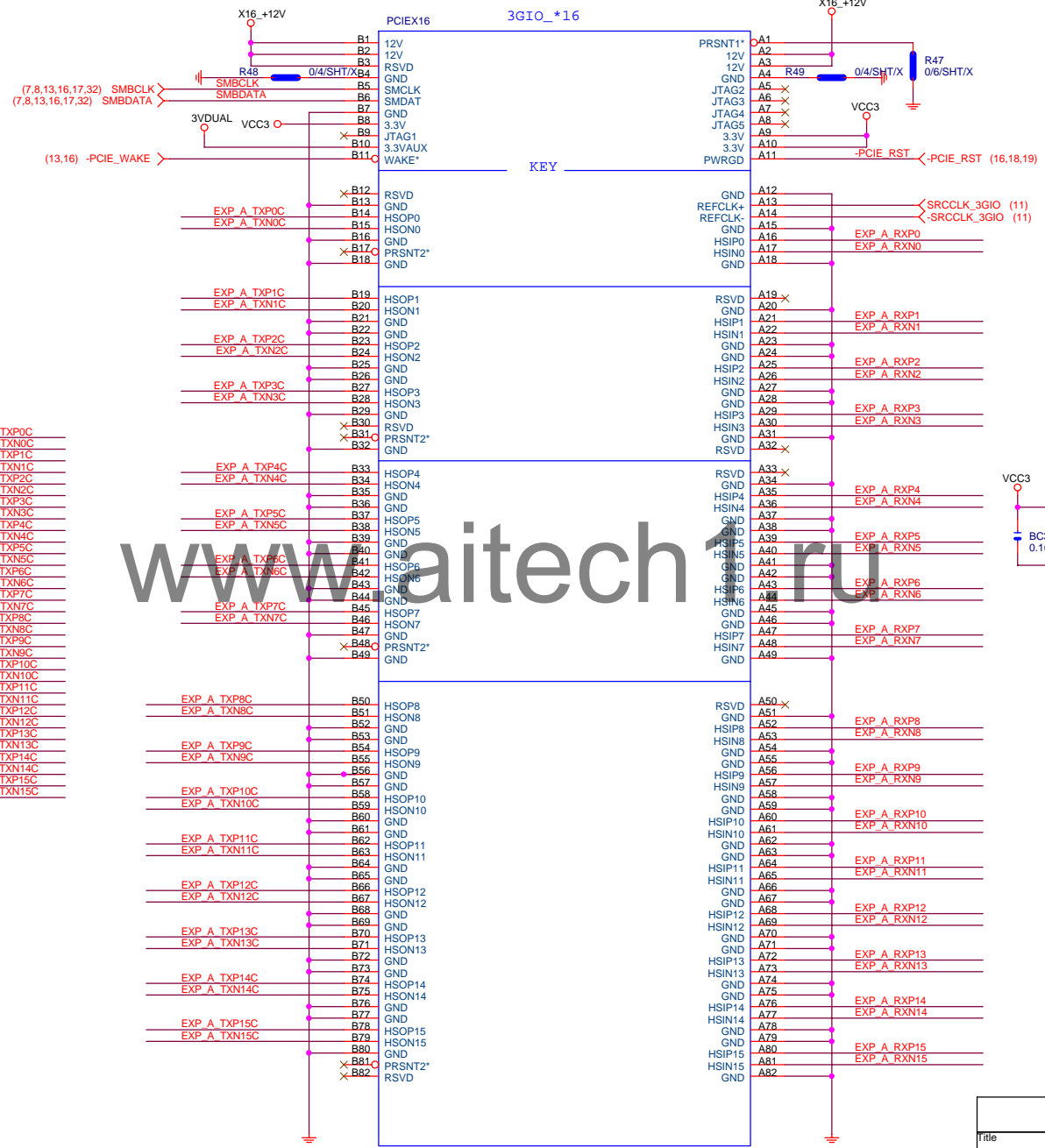




EXP A RXP0..15] >>> EXP_A_RXP0..15] (4)
EXP A RXN0..15] >>> EXP_A_RXN0..15] (4)

EXP A TXP0..15] >>> EXP_A_TXP0..15] (4)
EXP A TXN0..15] >>> EXP_A_TXN0..15] (4)

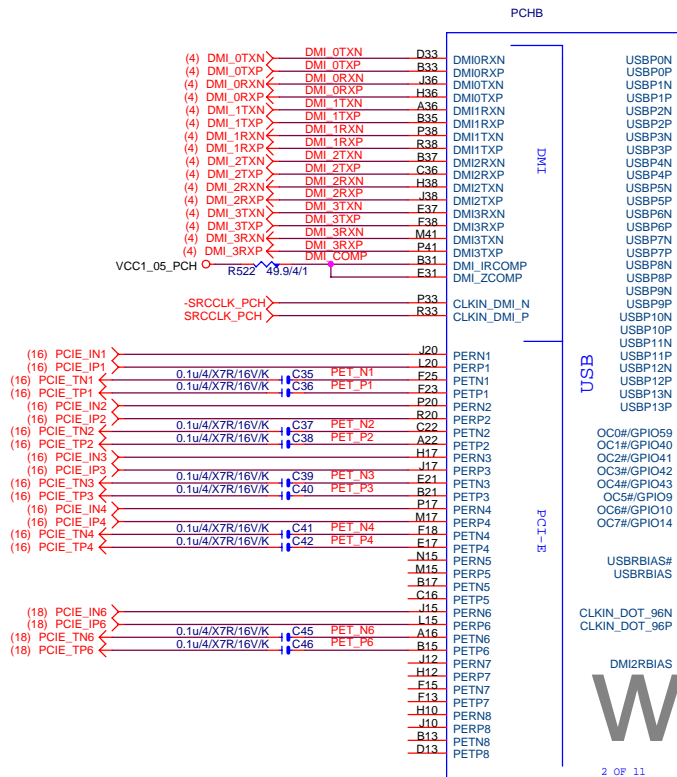
EXP A TXP0	C2	0.22u4/X5R/6.3V/K	EXP A TXP0C
EXP A TXN0	C3	0.22u4/X5R/6.3V/K	EXP A TXN0C
EXP A TXP1	C4	0.22u4/X5R/6.3V/K	EXP A TXP1C
EXP A TXN1	C5	0.22u4/X5R/6.3V/K	EXP A TXN1C
EXP A TXP2	C6	0.22u4/X5R/6.3V/K	EXP A TXP2C
EXP A TXN2	C7	0.22u4/X5R/6.3V/K	EXP A TXN2C
EXP A TXP3	C8	0.22u4/X5R/6.3V/K	EXP A TXP3C
EXP A TXN3	C9	0.22u4/X5R/6.3V/K	EXP A TXN3C
EXP A TXP4	C10	0.22u4/X5R/6.3V/K	EXP A TXP4C
EXP A TXN4	C11	0.22u4/X5R/6.3V/K	EXP A TXN4C
EXP A TXP5	C12	0.22u4/X5R/6.3V/K	EXP A TXP5C
EXP A TXN5	C13	0.22u4/X5R/6.3V/K	EXP A TXN5C
EXP A TXP6	C14	0.22u4/X5R/6.3V/K	EXP A TXP6C
EXP A TXN6	C15	0.22u4/X5R/6.3V/K	EXP A TXN6C
EXP A TXP7	C16	0.22u4/X5R/6.3V/K	EXP A TXP7C
EXP A TXN7	C17	0.22u4/X5R/6.3V/K	EXP A TXN7C
EXP A TXP8	C18	0.22u4/X5R/6.3V/K	EXP A TXP8C
EXP A TXN8	C19	0.22u4/X5R/6.3V/K	EXP A TXN8C
EXP A TXP9	C20	0.22u4/X5R/6.3V/K	EXP A TXP9C
EXP A TXN9	C21	0.22u4/X5R/6.3V/K	EXP A TXN9C
EXP A TXP10	C22	0.22u4/X5R/6.3V/K	EXP A TXP10C
EXP A TXN10	C23	0.22u4/X5R/6.3V/K	EXP A TXN10C
EXP A TXP11	C24	0.22u4/X5R/6.3V/K	EXP A TXP11C
EXP A TXN11	C25	0.22u4/X5R/6.3V/K	EXP A TXN11C
EXP A TXP12	C26	0.22u4/X5R/6.3V/K	EXP A TXP12C
EXP A TXN12	C27	0.22u4/X5R/6.3V/K	EXP A TXN12C
EXP A TXP13	C28	0.22u4/X5R/6.3V/K	EXP A TXP13C
EXP A TXN13	C29	0.22u4/X5R/6.3V/K	EXP A TXN13C
EXP A TXP14	C30	0.22u4/X5R/6.3V/K	EXP A TXP14C
EXP A TXN14	C31	0.22u4/X5R/6.3V/K	EXP A TXN14C
EXP A TXP15	C32	0.22u4/X5R/6.3V/K	EXP A TXP15C
EXP A TXN15	C33	0.22u4/X5R/6.3V/K	EXP A TXN15C



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PCI-E/16X-164P/BU-297C/RIGHT PUSH

GIGABYTE Technology			
Title			
PCI EXPRESS * 16			
Size	Document Number	Rev	
Custom	GA-Q67M-D2H-B3	1.0	
Date:	Friday, April 15, 2011	Sheet	9 of 32

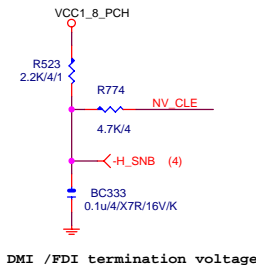
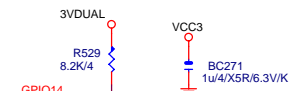


USB

PCI-E

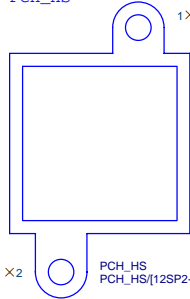
BD82Q67/B3/S[10HB1-030Q67-40R]

2 OF 11

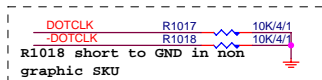


DMI /FDI termination voltage

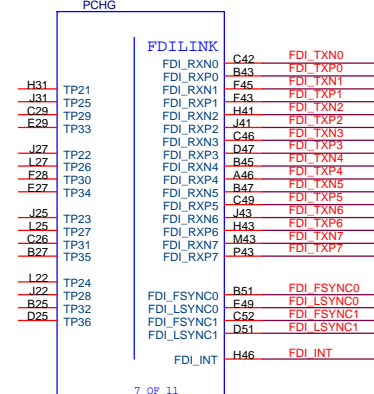
PCH_HS



PCH_HS
PCH_HS[12SP2-050073-11R_12SP2-050073-12R_12SP2-050073-13R]

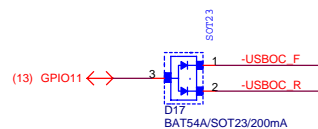


R1018 short to GND in non graphic SKU

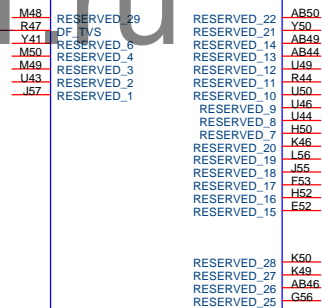


BD82Q67/B3/S[10HB1-030Q67-40R]

FDI_TXP0[.7] (4)
FDI_TXN0[.7] (4)



PCHE



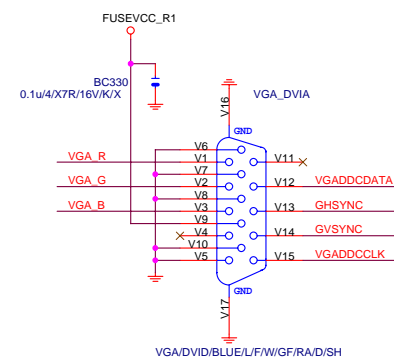
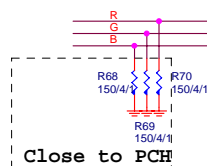
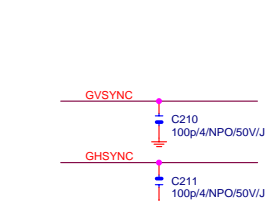
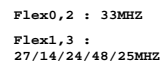
NVRAM

BD82Q67/B3/S[10HB1-030Q67-40R]

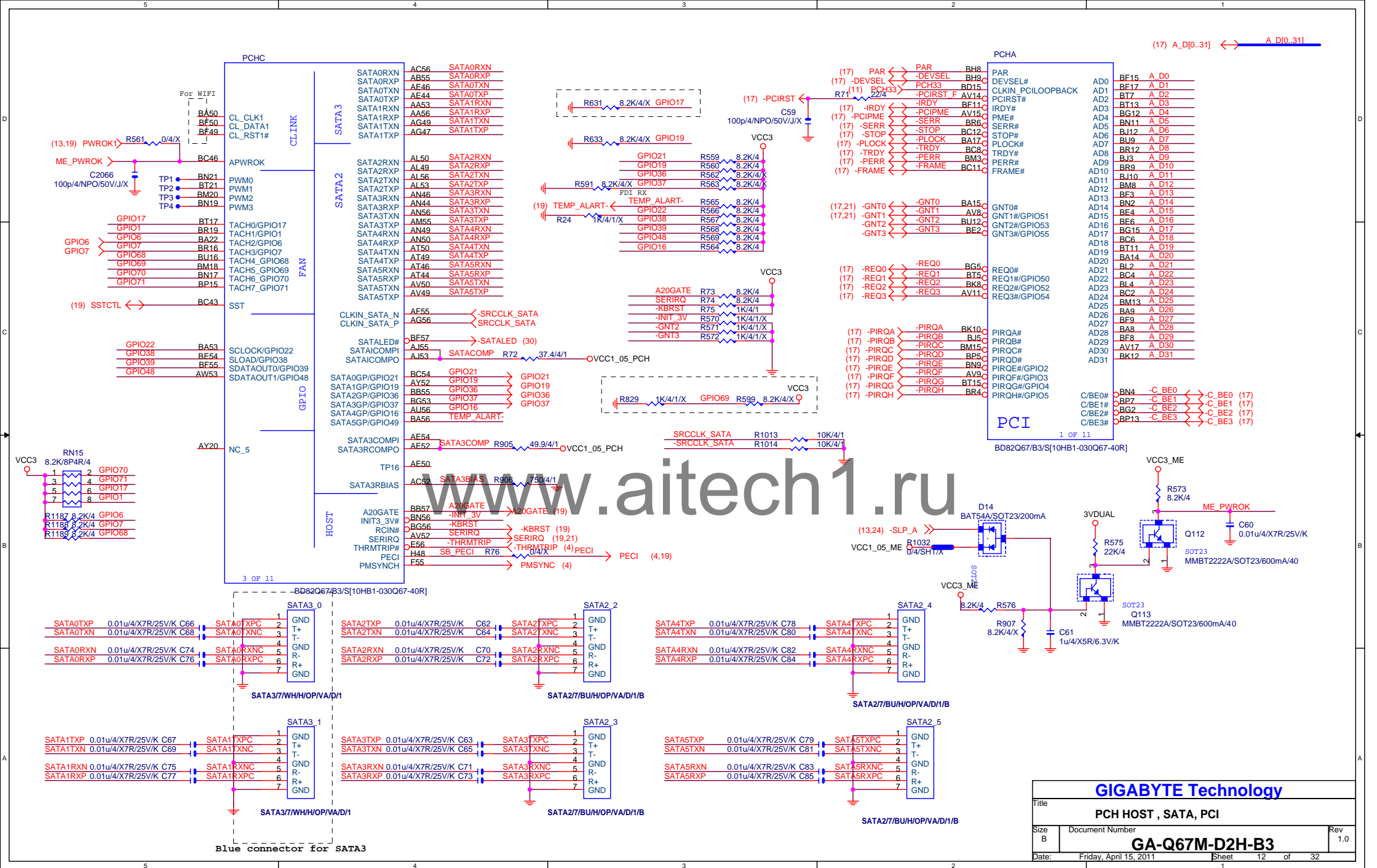
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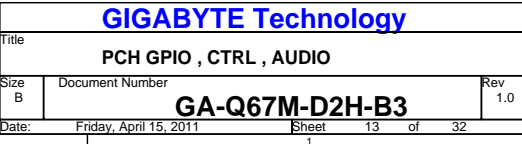
PCH FDI,DMI,USB ,PCIE,NVRAM

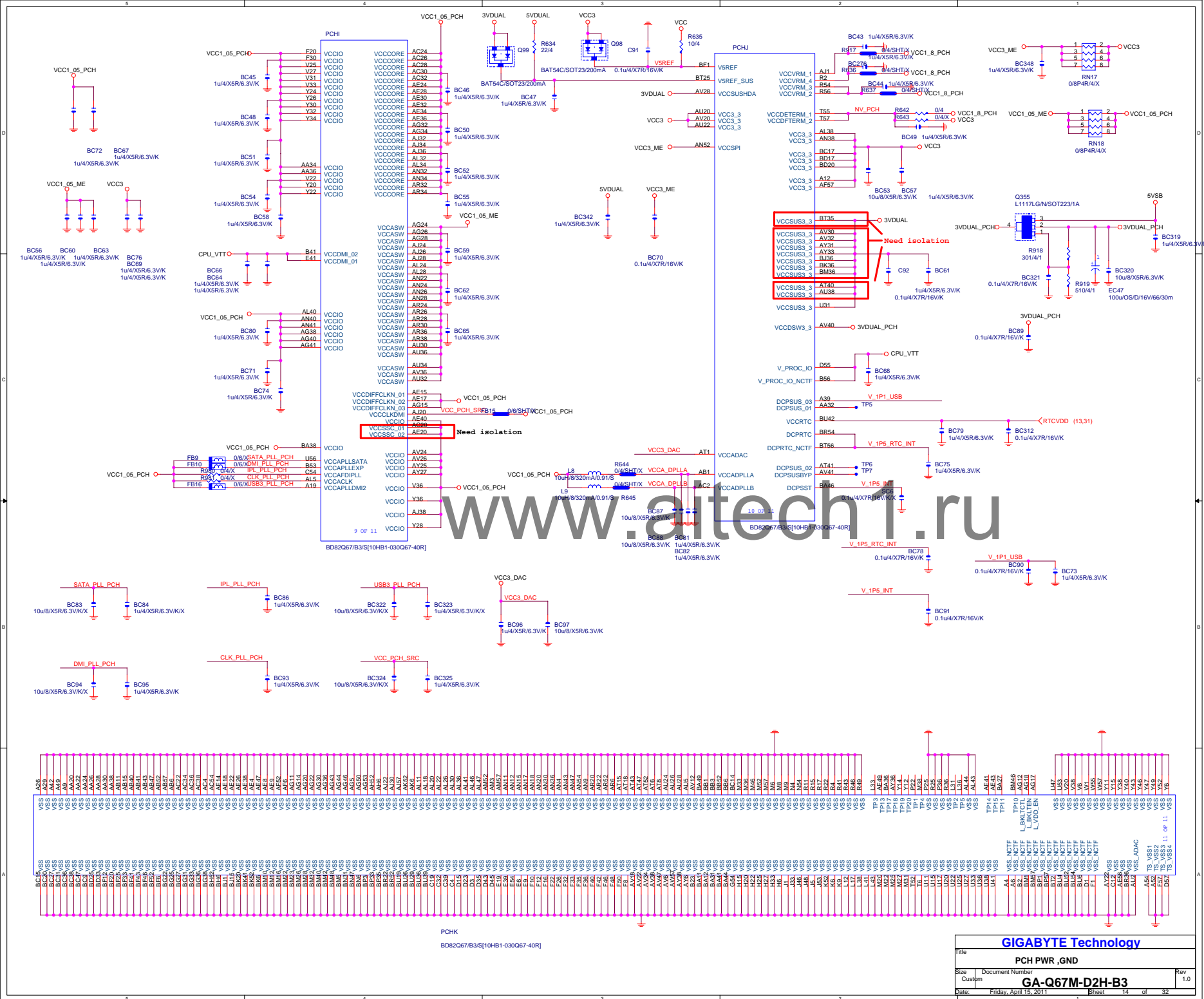
GA-Q67M-D2H-B3



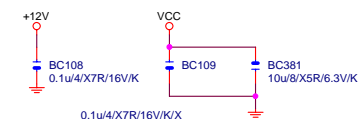
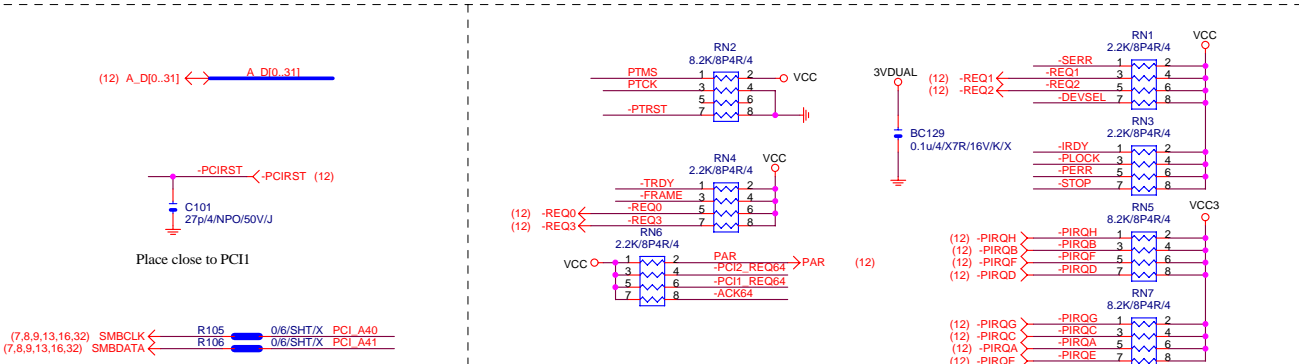
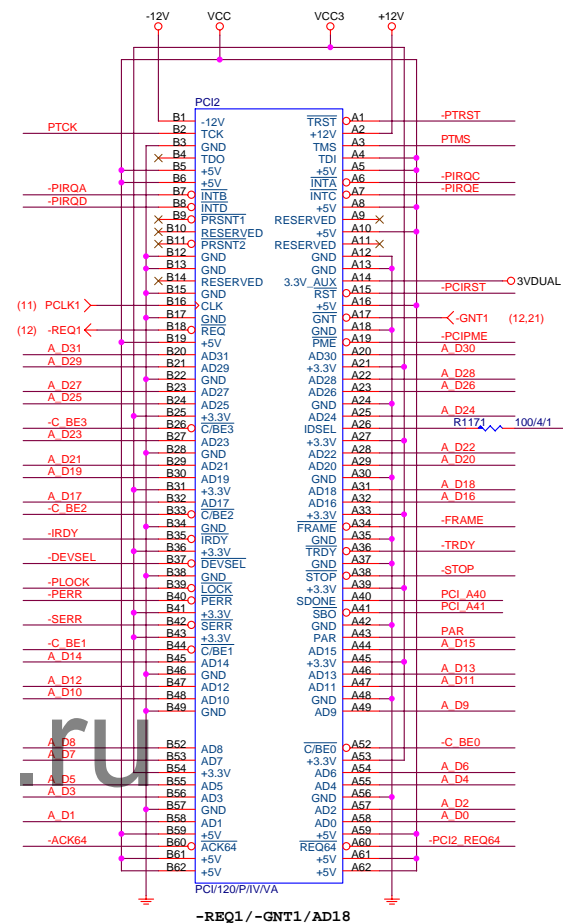
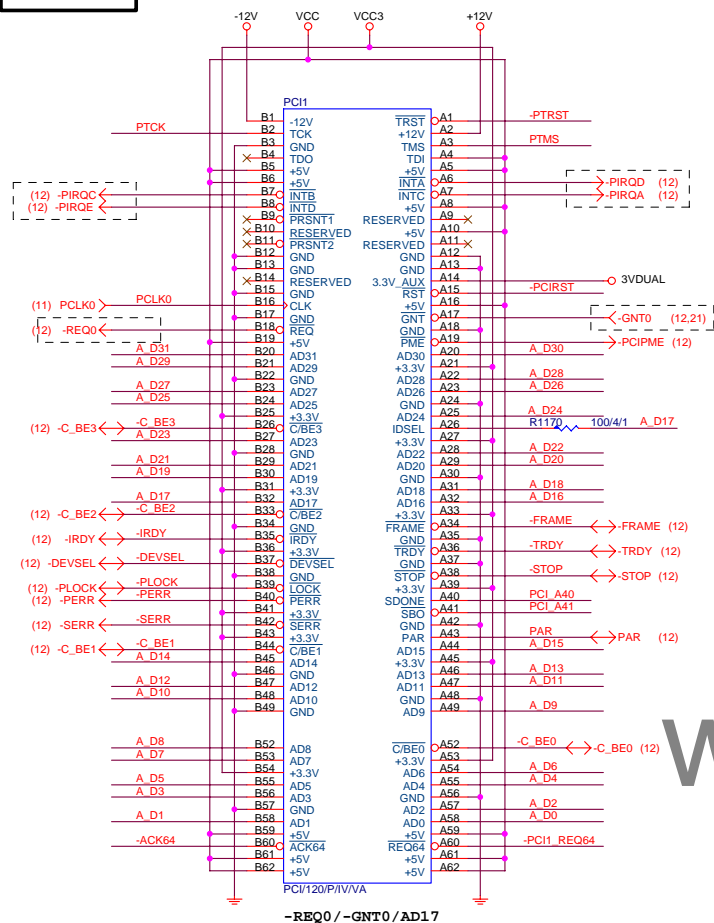
<h1 style="text-align: center; color: blue;">GIGABYTE Technology</h1>			
<p>Title PCH DISPLAY ,CLK BUFFER</p>			
Size	Document Number	Rev	
Custom	GA-Q67M-D2H-B3	1.0	
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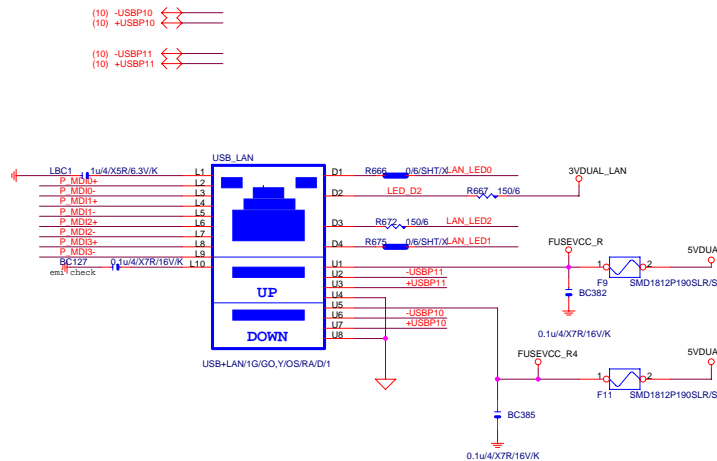
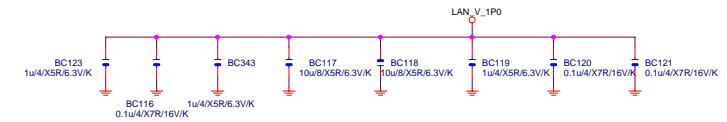
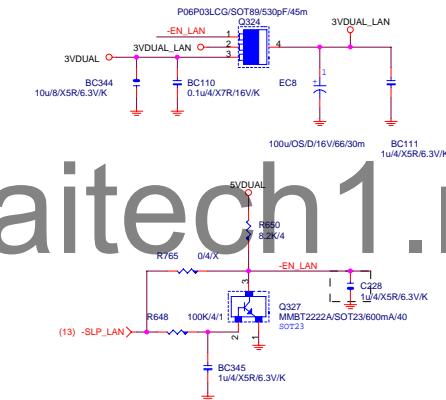
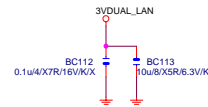


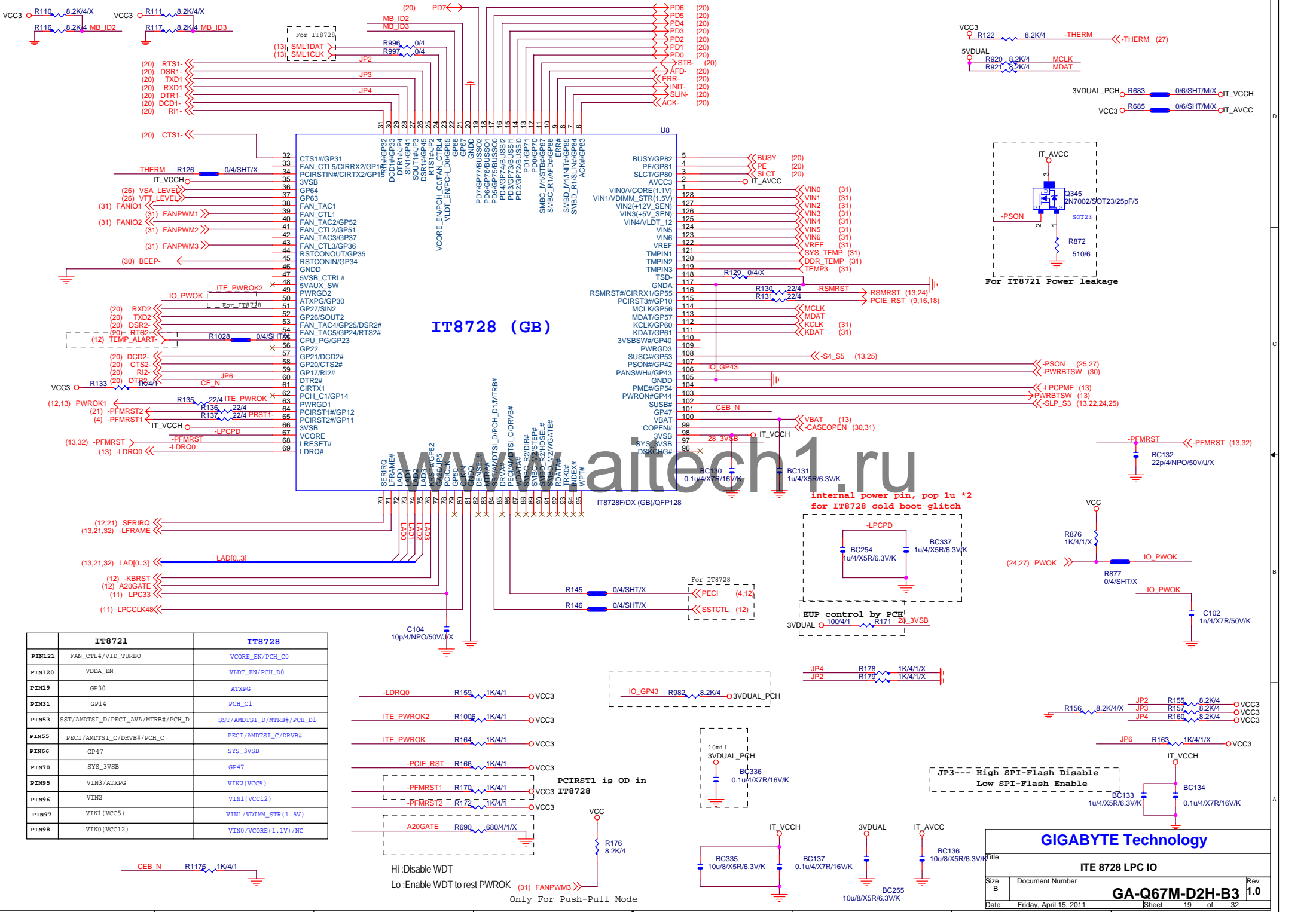




PCI SLOT

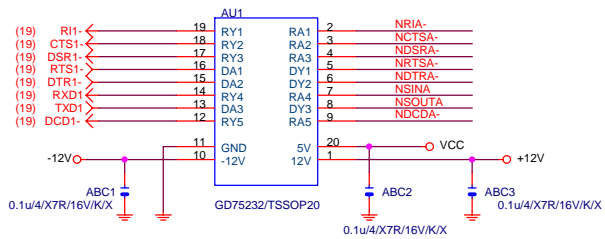




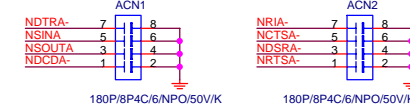
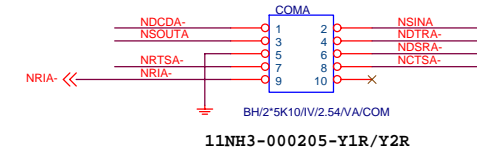
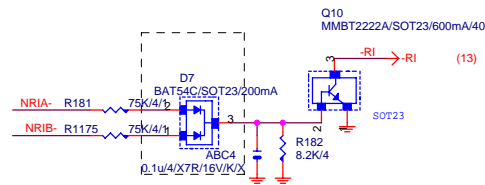


IT8721	IT8728
PIN121	FAN_CTL4/VID_TURBO
PIN120	VDDA_EN
PIN19	GP30
PIN31	GP14
PIN53	SST/AMDTSI_D/PECI_AVA/MTRB#/PCH_D
PIN55	PECI/AMDTSI_C/DRVB#/PCH_C
PIN66	GP47
PIN70	SYS_3VSB
PIN95	VIN3/ATXPG
PIN96	VIN2
PIN97	VIN1(VCC5)
PIN98	VIN0(VCC12)

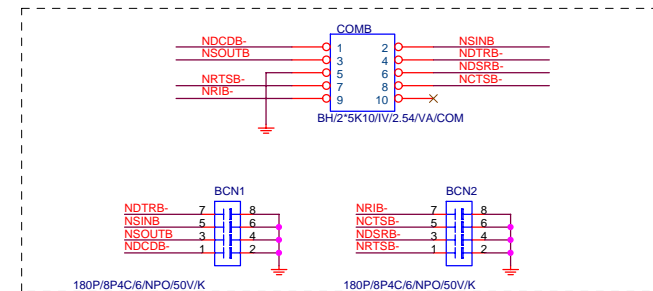
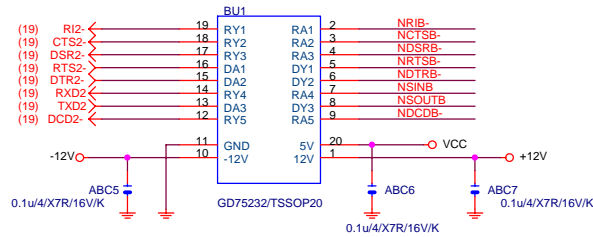
COMA



COM RI

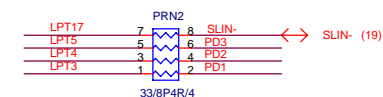
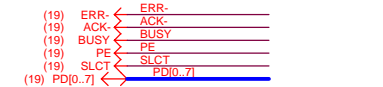
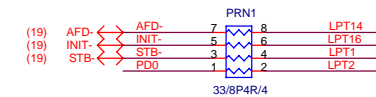
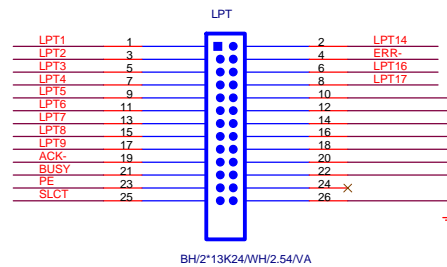
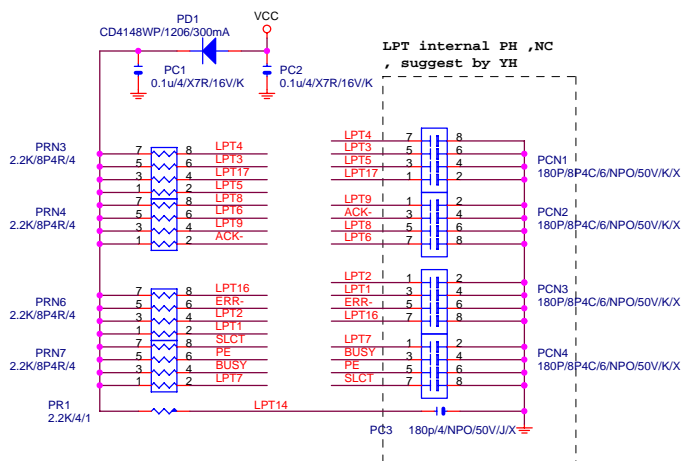


COMB



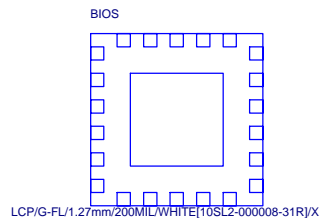
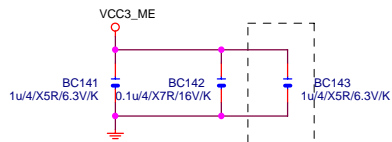
PLACE NEAR COM CONNECTOR

LPT PORT



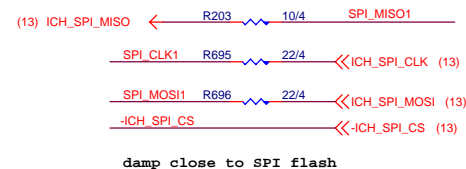
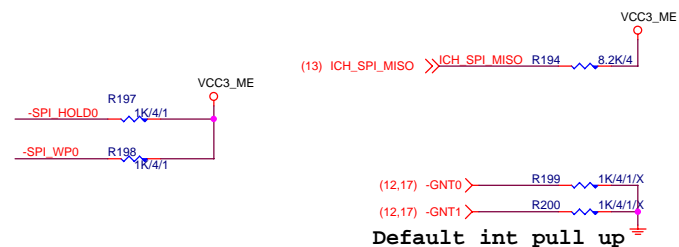
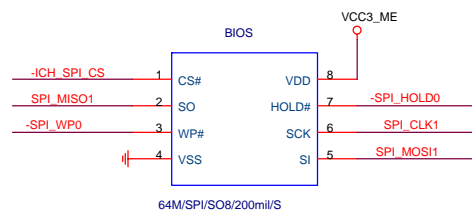
GIGABYTE Technology

COM A/B & LPT			
Title	COM A/B & LPT		
Size	Document Number	Rev	
Custom			
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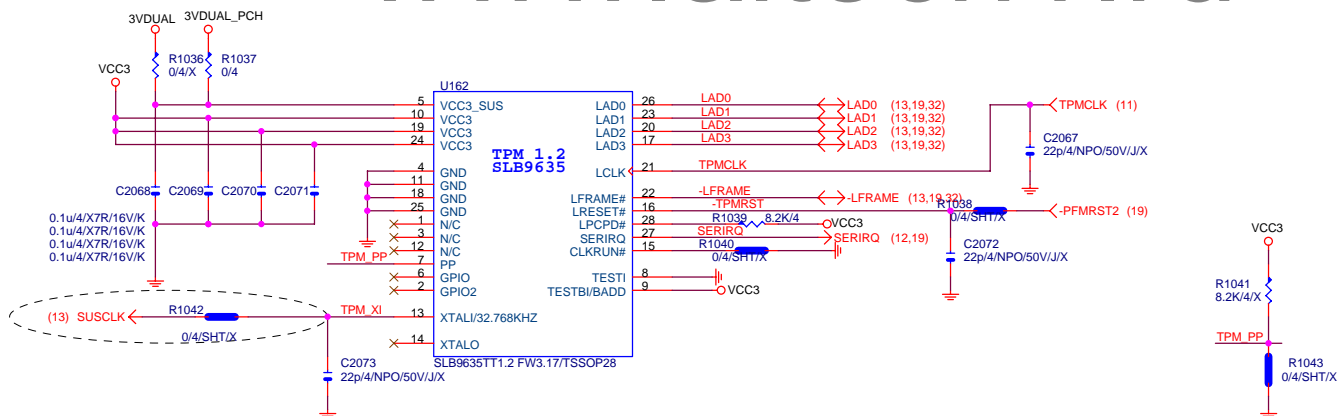
BOOT DEVICE	GNT1	GP19
LPC	0	0
PCI	1	0
SPI	1	1

1 means floating
0 means PD 1K



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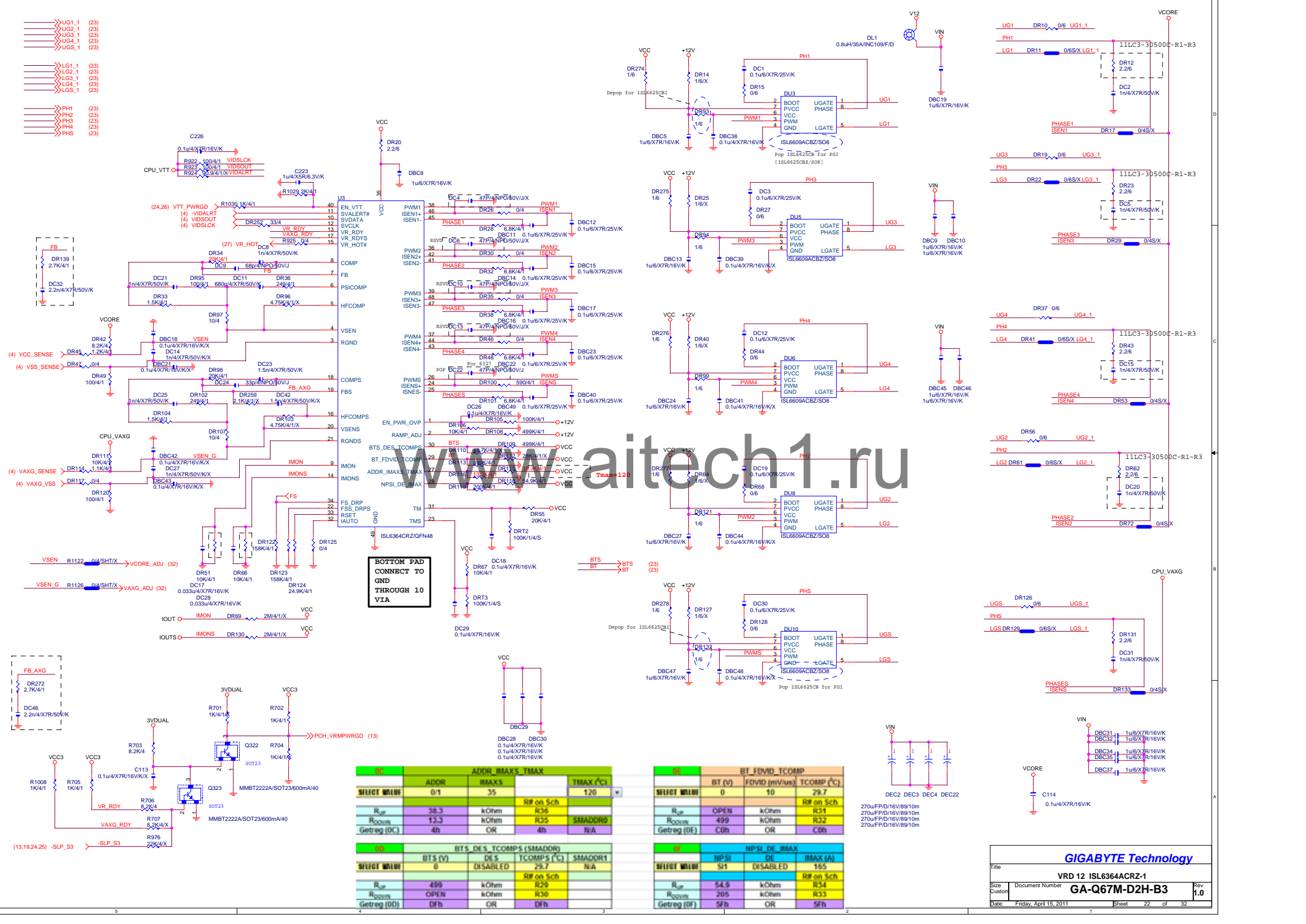
TPM



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Title				BIOS
Size	Custom	Document Number	GA-Q67M-D2H-B3	
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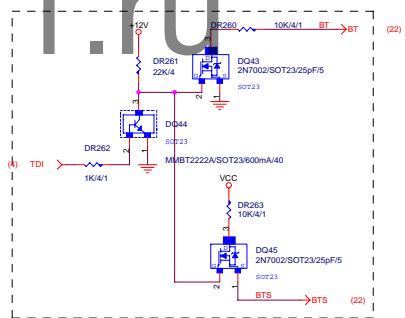
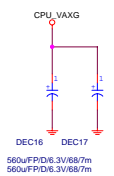
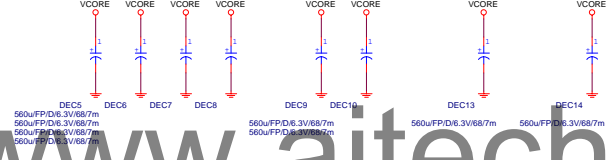
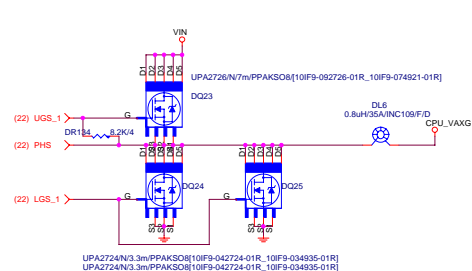
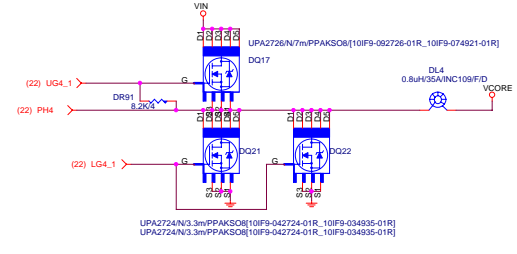
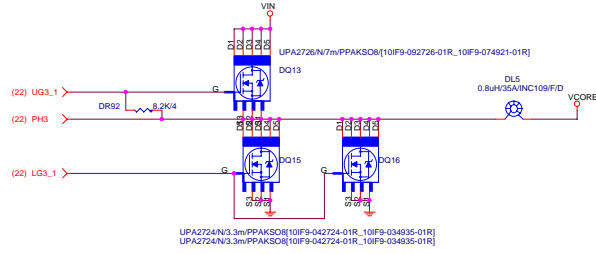
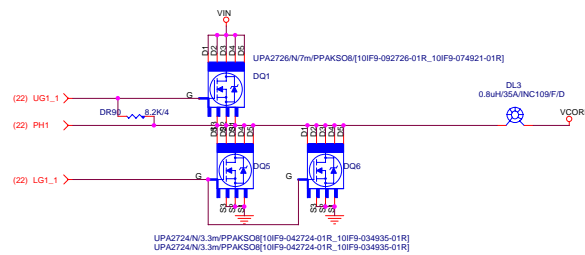
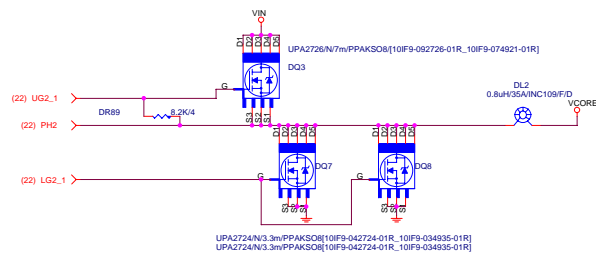


ISL6364CRZ-QFN48			
ADDR. MAXS. TMAX			
SELECT VALUE	ADDR	MAXS	TMAX (°C)
0/1	35		120
R _{DS(on)}	38.3	kOhm	R _{DS(on)} Sch
R _{DS(on)}	13.3	kOhm	R _{DS(on)} R35
Getreg (DC)	4h	OR	4h

BTS. DES. TCOMPS (SMADWR)			
SELECT VALUE	BTS (V)	DES	TCOMPS (°C)
0	DISABLED		29.7
R _{DS(on)}	499	kOhm	R _{DS(on)} Sch
R _{DS(on)}	205	kOhm	R _{DS(on)} R32
Getreg (DC)	5fh	OR	5fh

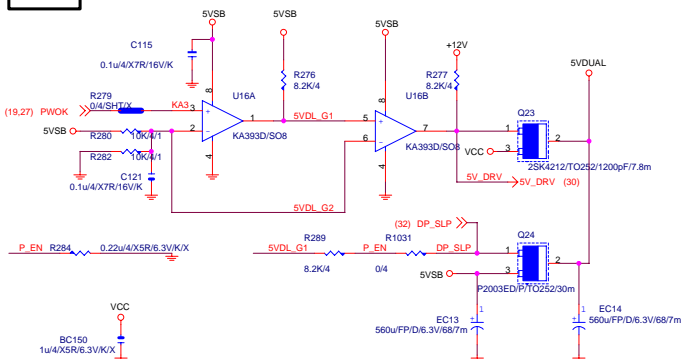
BT. FVID. TCOMP			
SELECT VALUE	BT (V)	FVID (mV)	TCOMP (°C)
0	10		29.7
R _{DS(on)}	OPEN	kOhm	R _{DS(on)} R31
R _{DS(on)}	499	kOhm	R _{DS(on)} R32
Getreg (DC)	5fh	OR	5fh

NP3. DE. MAX			
SELECT VALUE	NP3	DE	MAX (A)
0	DISABLED		165
R _{DS(on)}	54.5	kOhm	R _{DS(on)} Sch
R _{DS(on)}	205	kOhm	R _{DS(on)} R32
Getreg (DC)	5fh	OR	5fh

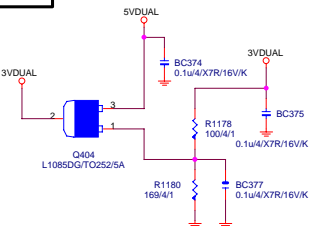


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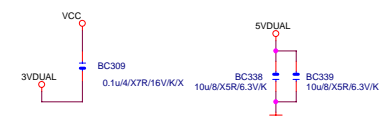
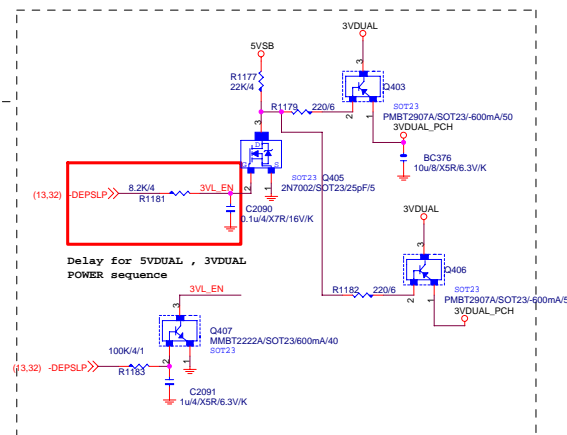
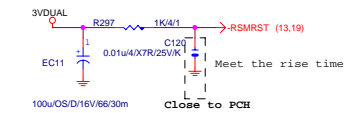
5VDUAL



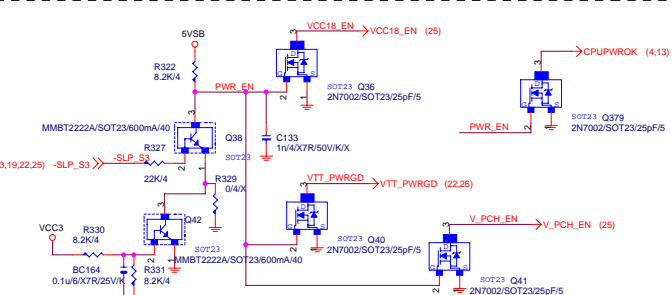
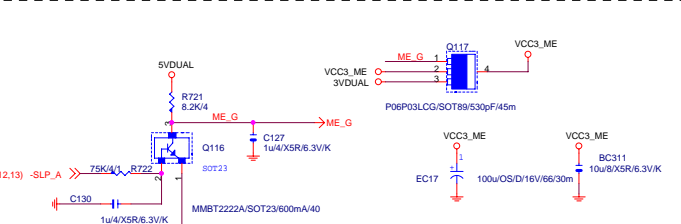
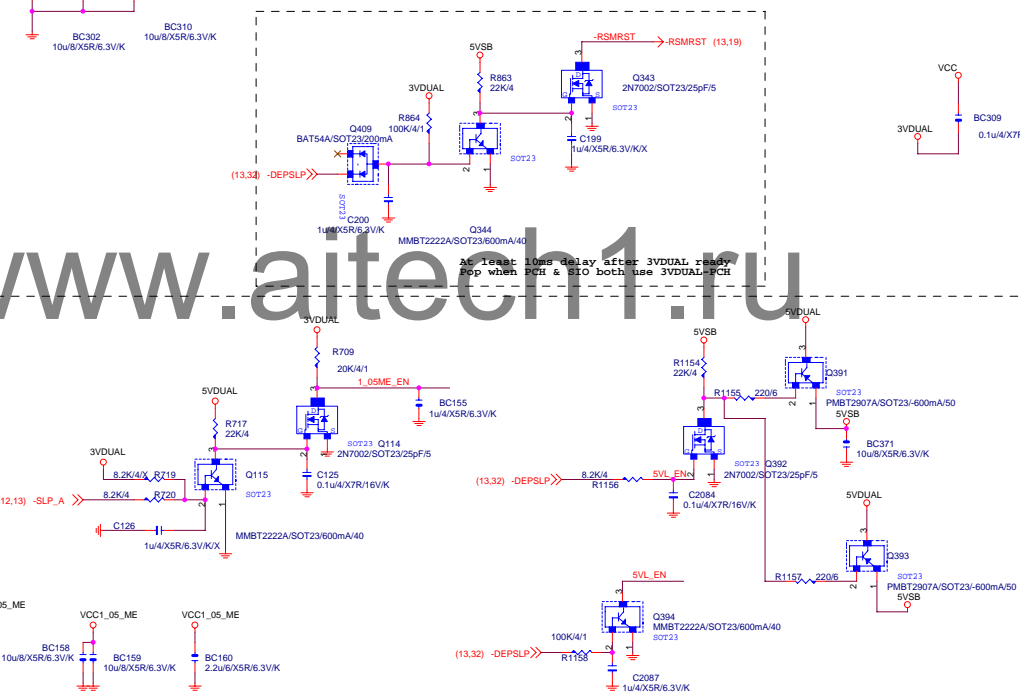
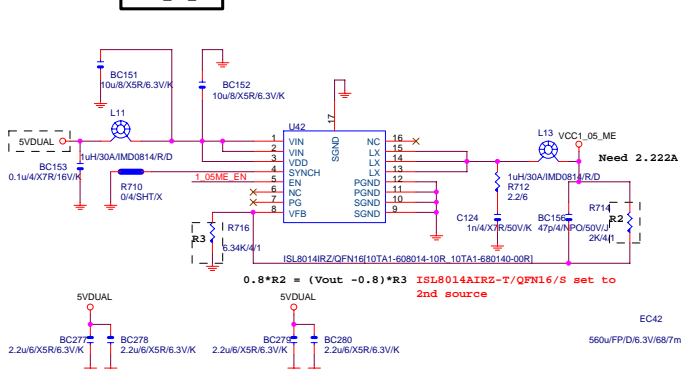
3VDUAL



Close up

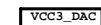
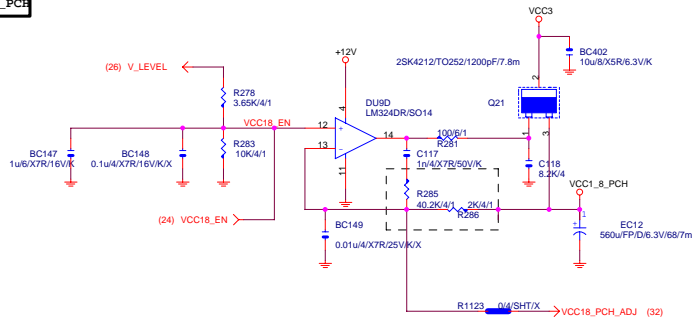


VCC1_05_ME

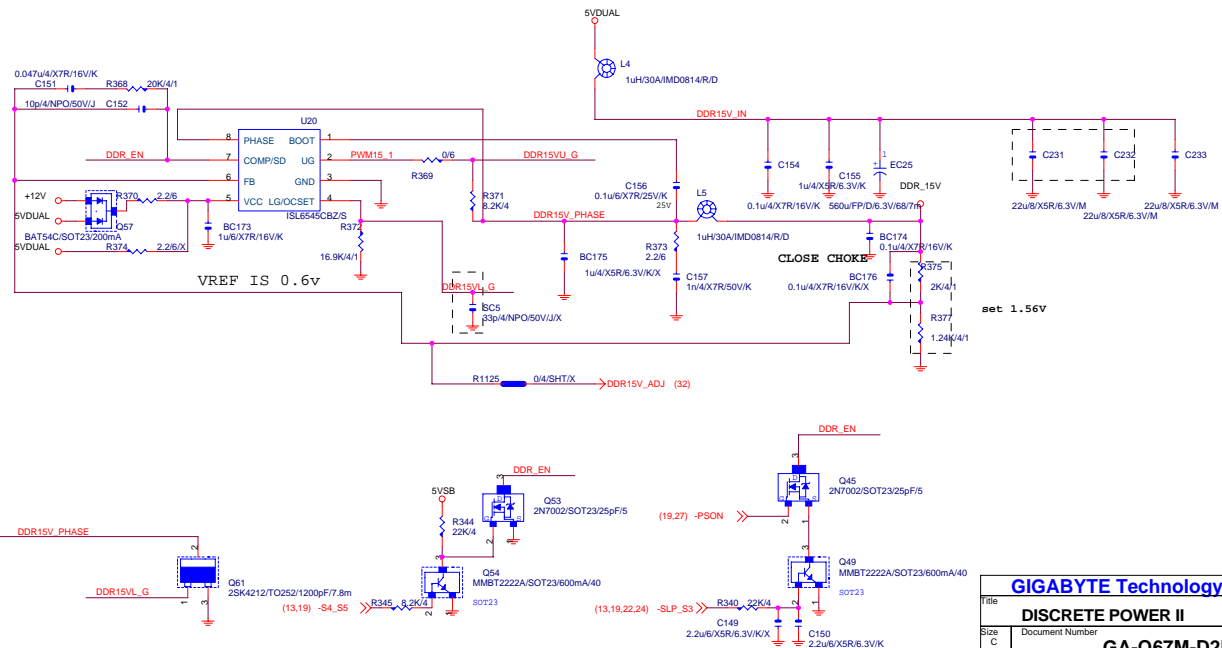
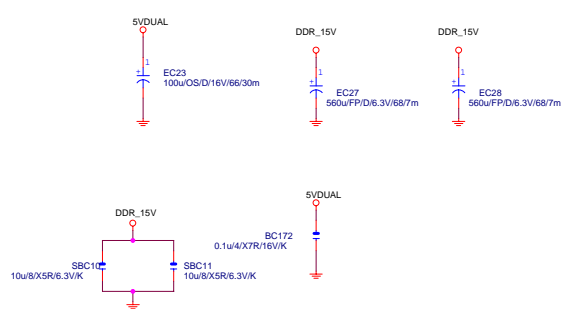
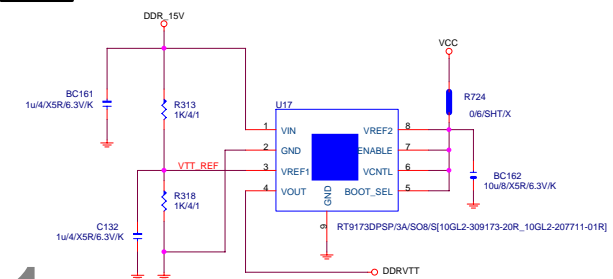
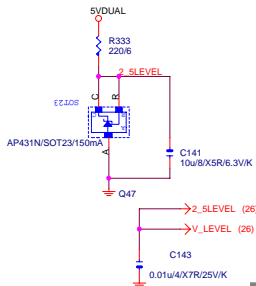
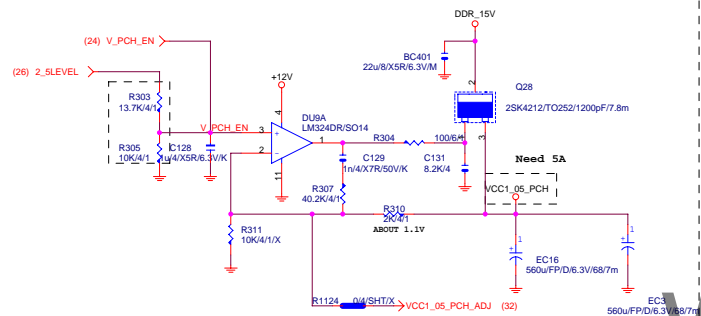
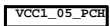
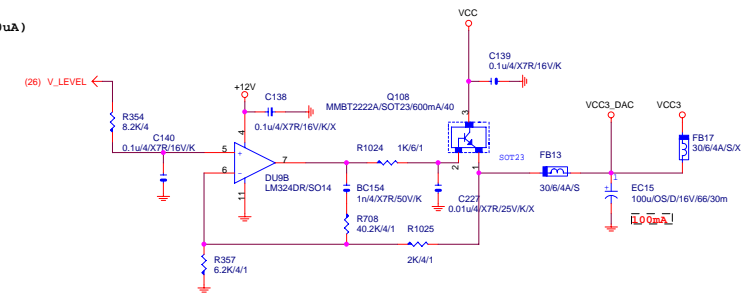


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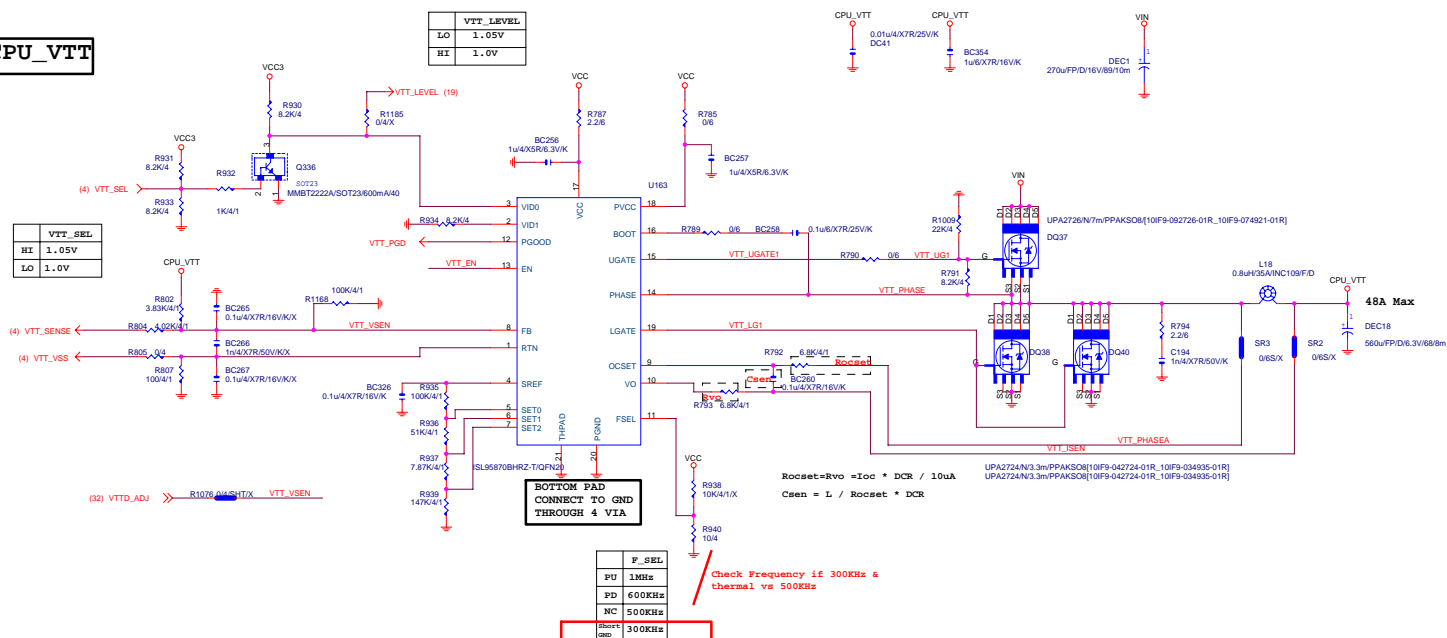
Title			
DISCRETE POWER			
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(3.3V/70mA+360uA)

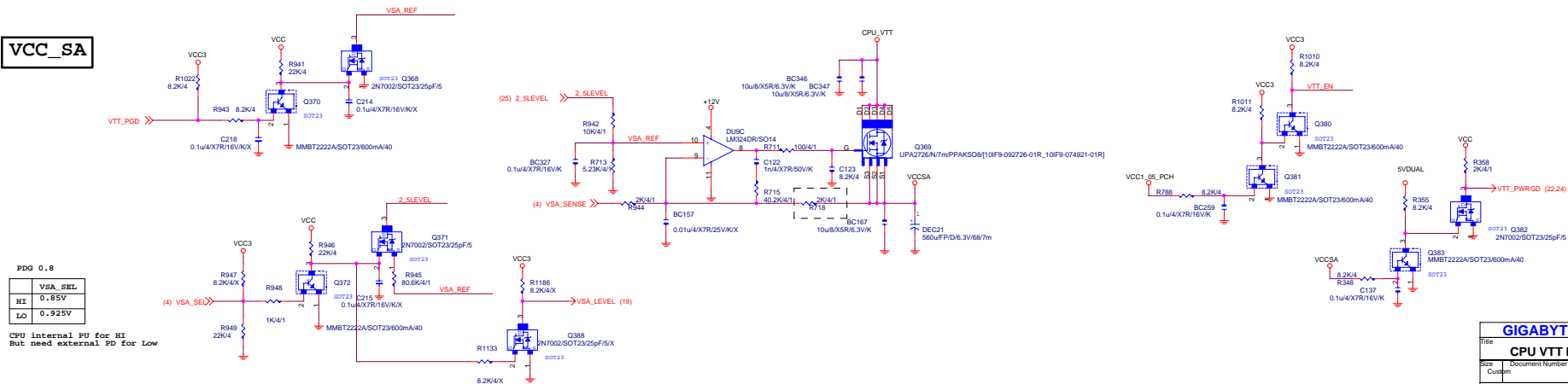


CPU_VTT

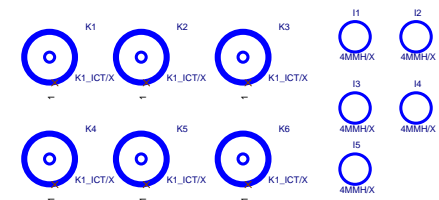
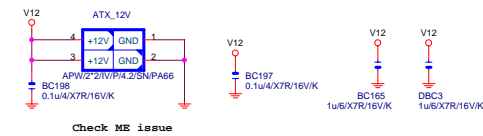
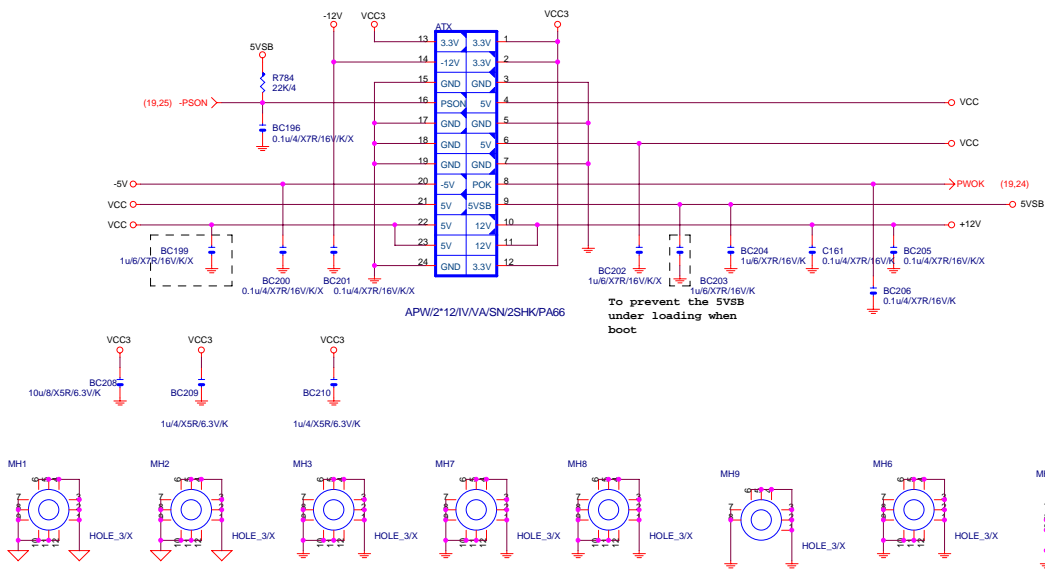


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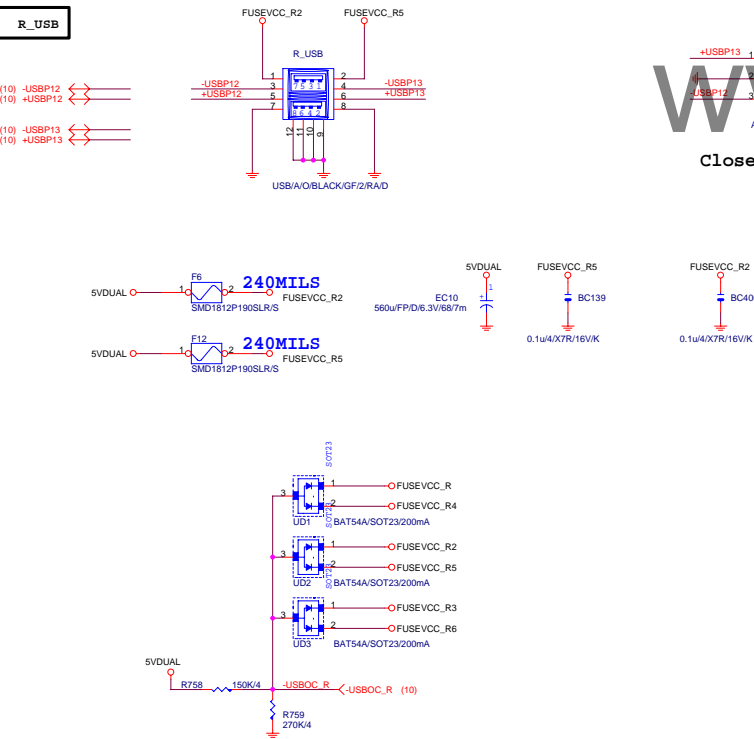
VCC_SA



ATX POWER CONNECTOR



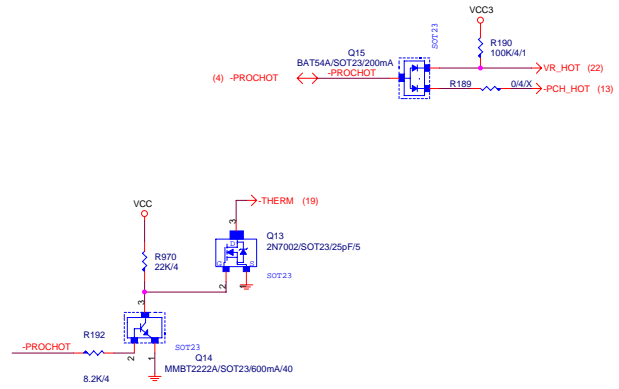
R_USB



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Close to connector

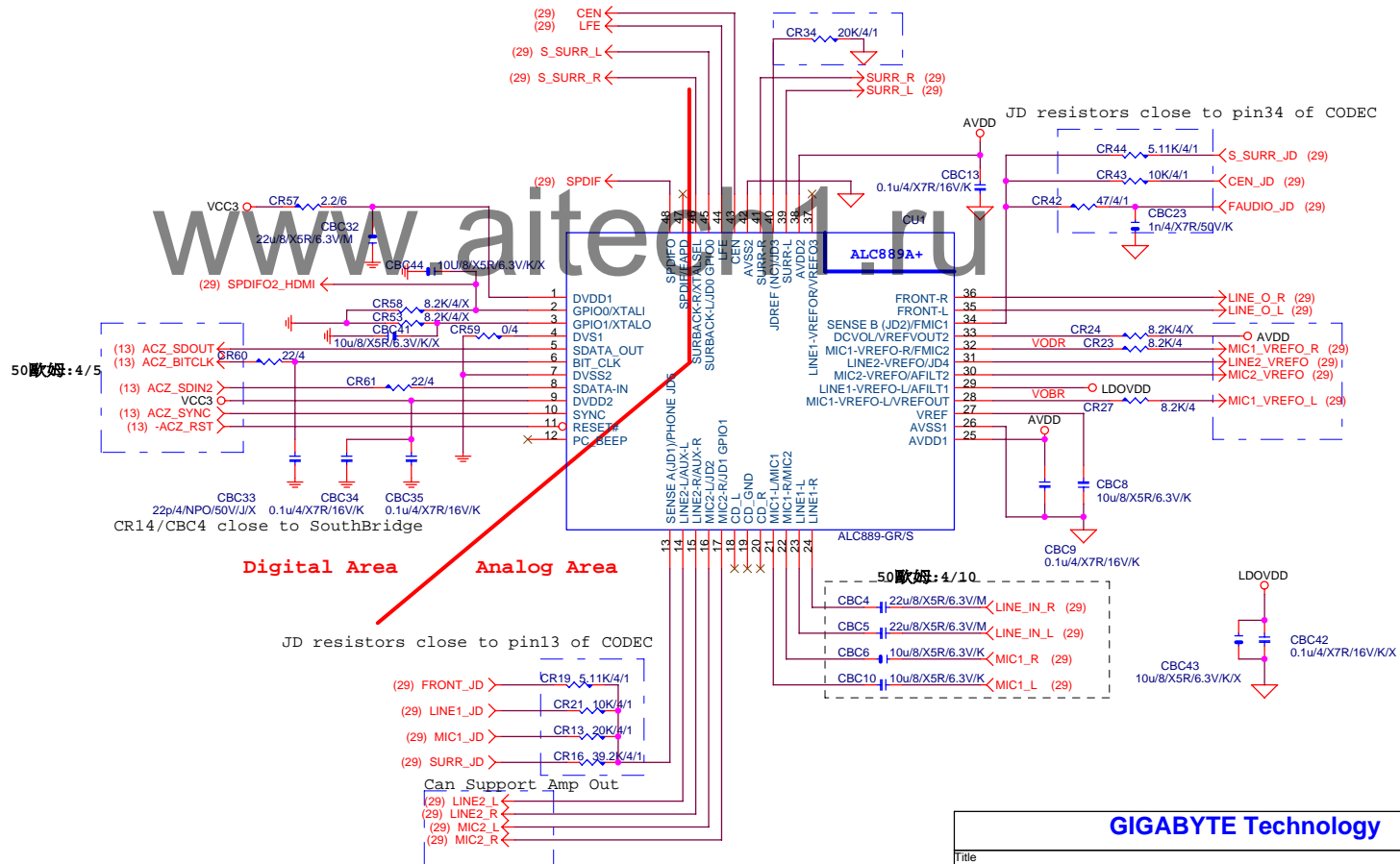
-PROHOT

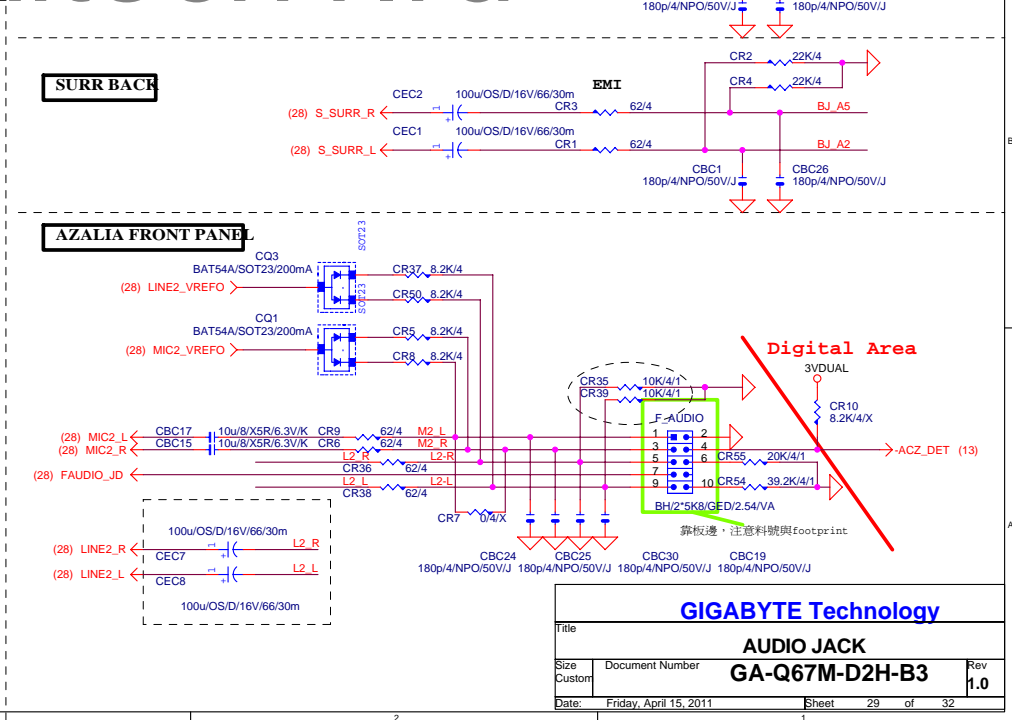
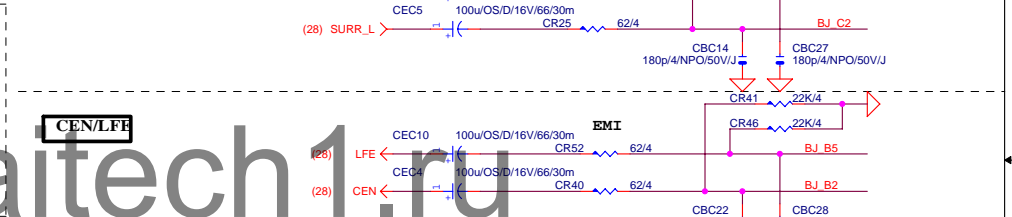
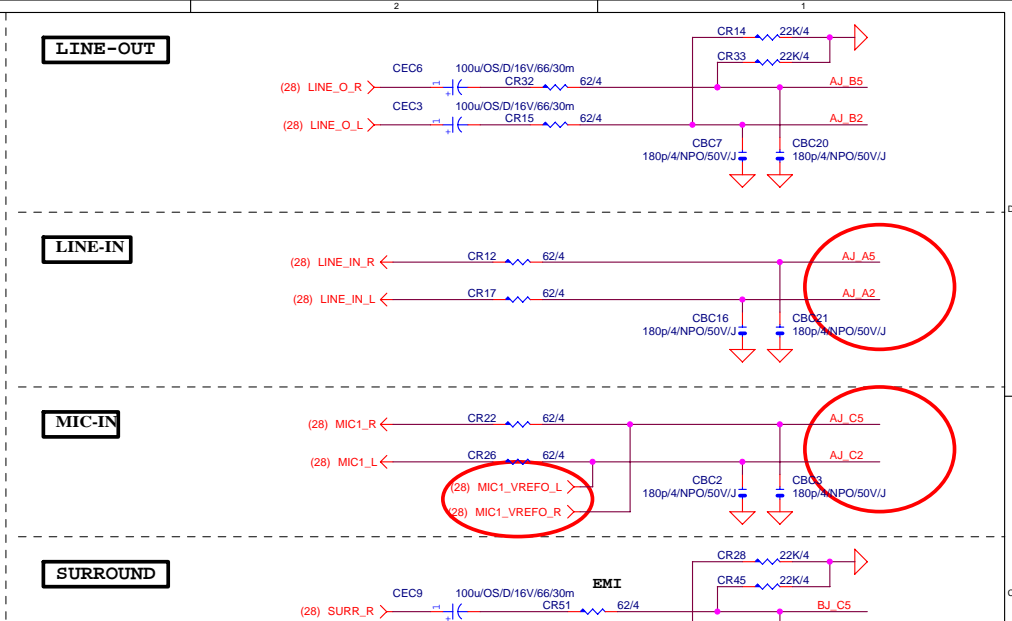


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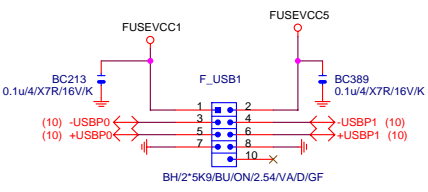
ATX / CLOCK GEN			
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	ALC883	ALC888-VA	ALC888B	ALC888-VD	ALC892 ALC887-VD	ALC889	ALC889A	ALC892R
PIN2	X	X	X	X	X	X	CR58=8.2K/4	X
PIN3	X	X	X	CBC41=10U	CBC41=10U	X	CR53=8.2K/4	CBC41=10U
PIN4	CR59=0/4	CR59=0/4	X	X	X	CR59=0/4	CR59=0/4	X
PIN29(For MIC Vref)	X	X	X	X	X	X	X	
* PIN33	CR24=8.2K	X	X	X	X	X	X	X
PIN40	20K/1%	20K/1%	20K/1%	20K/1%	20K/1%	20K/1%	20K/0.1%	20K/1%
SPDIF-SHARE	CR56=0/4	CR56=0/4	CR63=0/4	CR56=0/4	CR63=0/4	CR56=0/4	CR63=0/4	
Audio input Cap : CBC4/CBC5/CBC6/ CBC10/CBC15/CBC17	4.7uF /X5R	4.7uF /X5R	4.7uF /X5R	4.7uF /X5R	4.7uF /X5R	10uF /X5R	4.7uF /X5R	4.7uF /X5R
Audio I/O resistors : CR32/CR15/CR12/CR17/ CR22/CR26/CR51/CR25/ CR52/CR40/CR3/CR1/ CR9/CR6/CR36/CR38	75 ohm	75 ohm	75 ohm	75 ohm	75 ohm	66 ohm or lower	75 ohm	75 ohm
Embedded LDO: CR64/CR66	X	X	X	Stuff	Stuff	X	X	X
External LDO : CD1/CQ4/CQ5/CR65	Stuff	Stuff	Stuff	X	X	Stuff	Stuff	Stuff

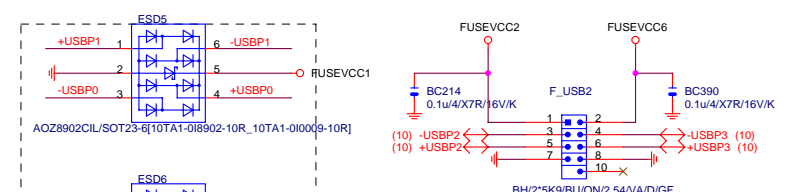




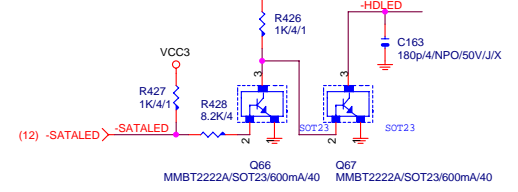
FRONT USB1



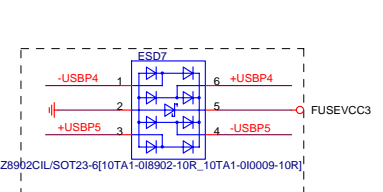
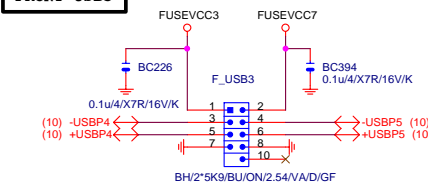
FRONT USB2



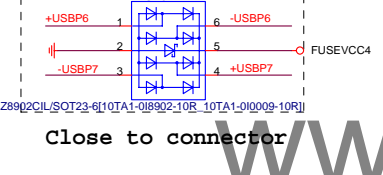
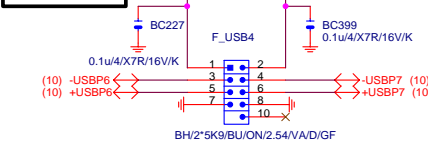
SATA LED



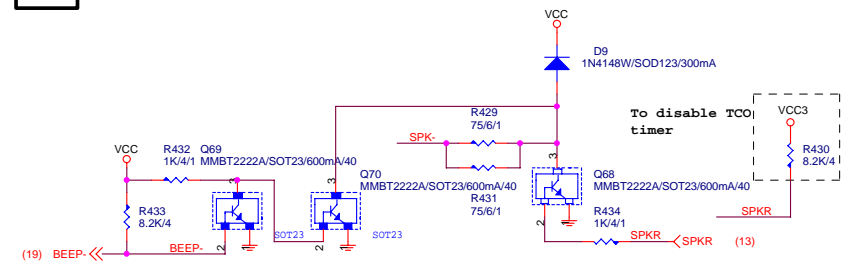
FRONT USB3



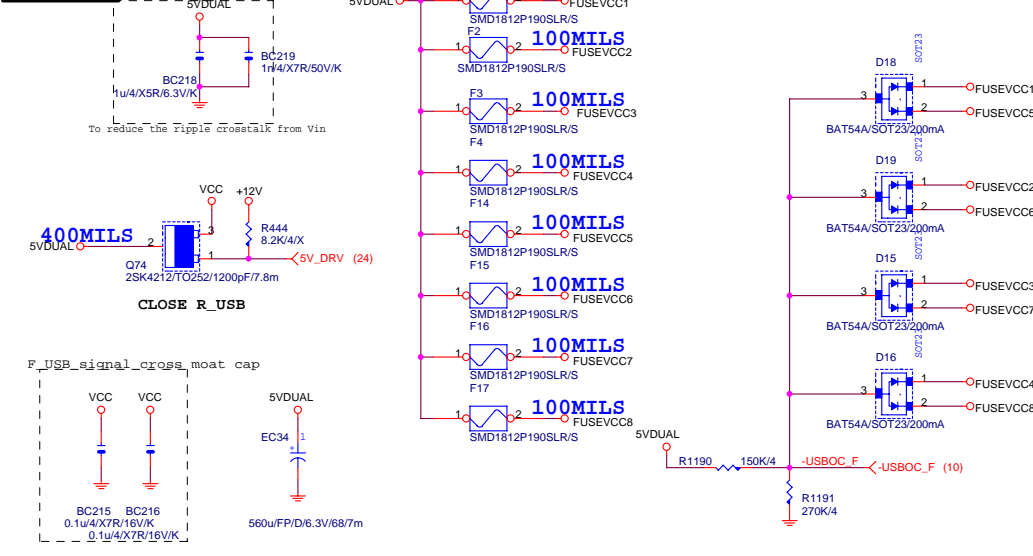
FRONT USB4



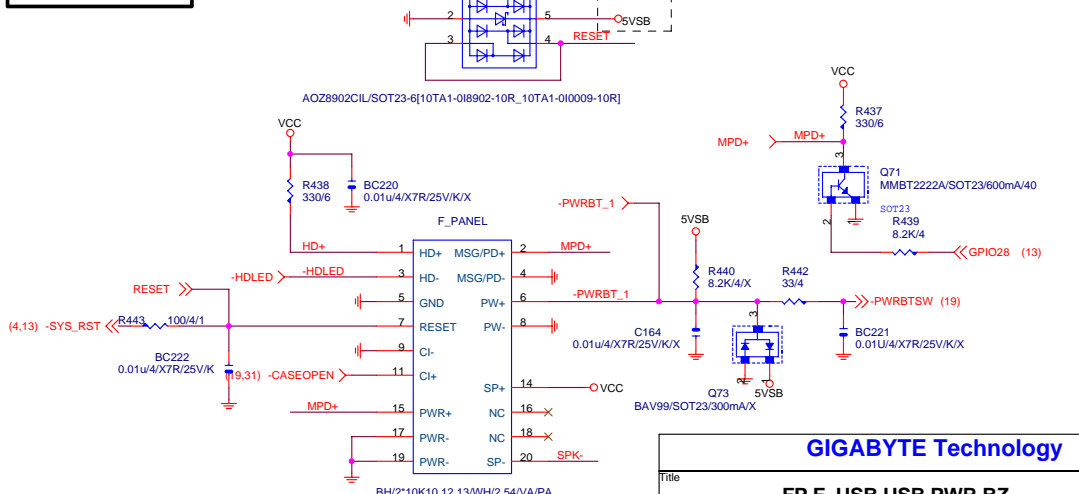
SPKR



F_USB POWER

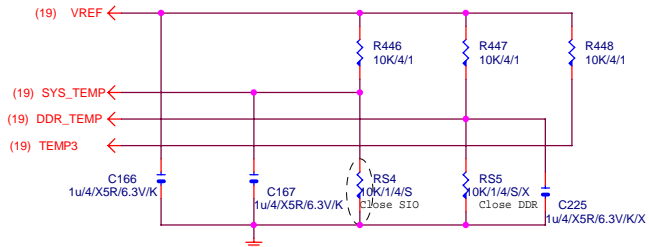


INTEL FRONT PANEL

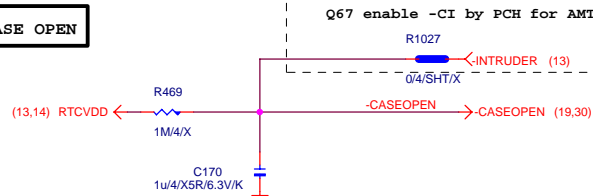


GIGABYTE Technology			
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TEMP H/W MONITOR

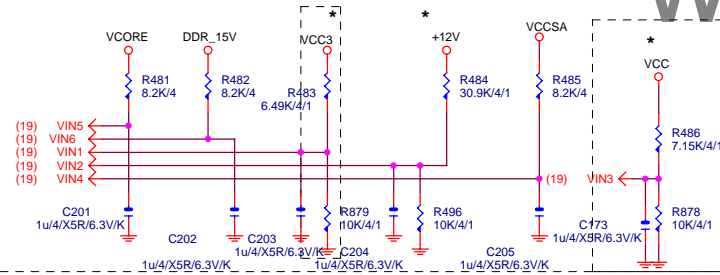


CASE OPEN

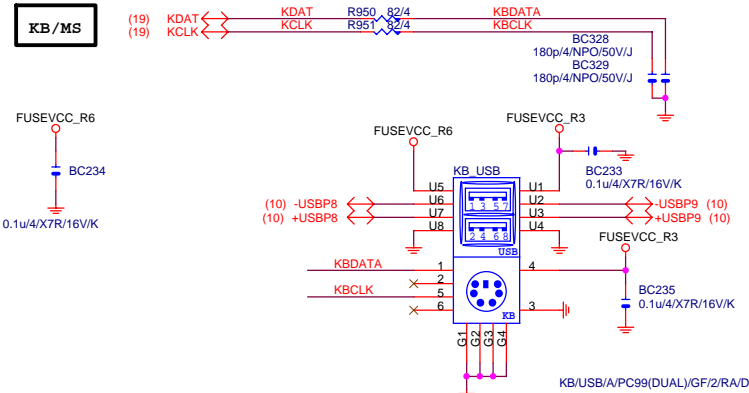


Case Open Circuits

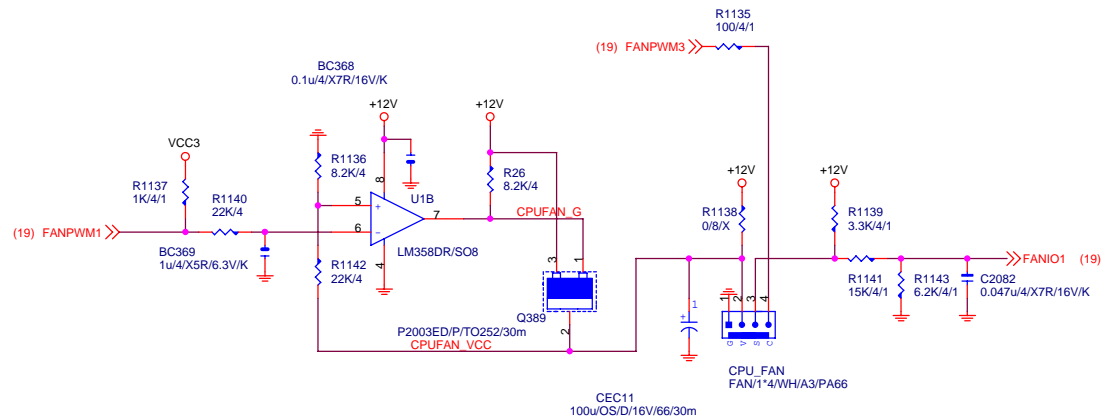
VOLTAGE-- H/W MONITOR



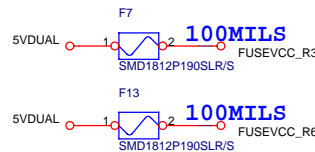
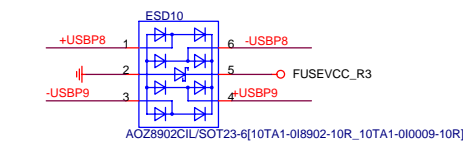
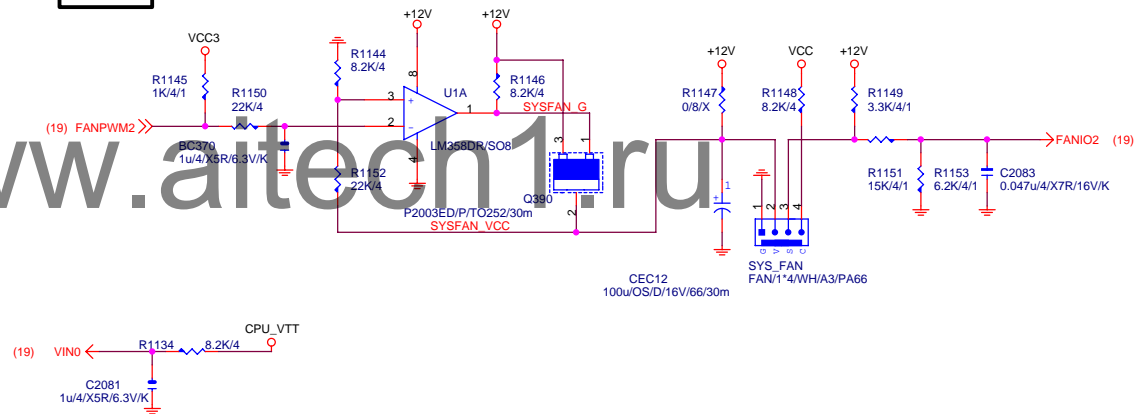
KB/MS



CPU SMART FAN



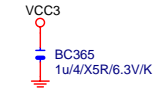
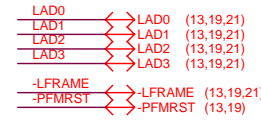
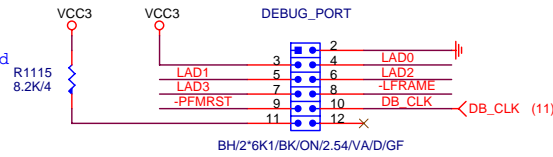
SYS FAN



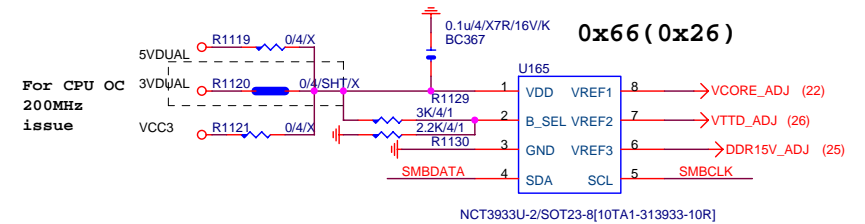
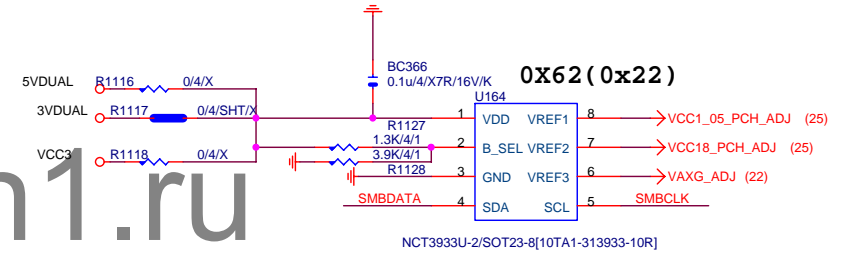
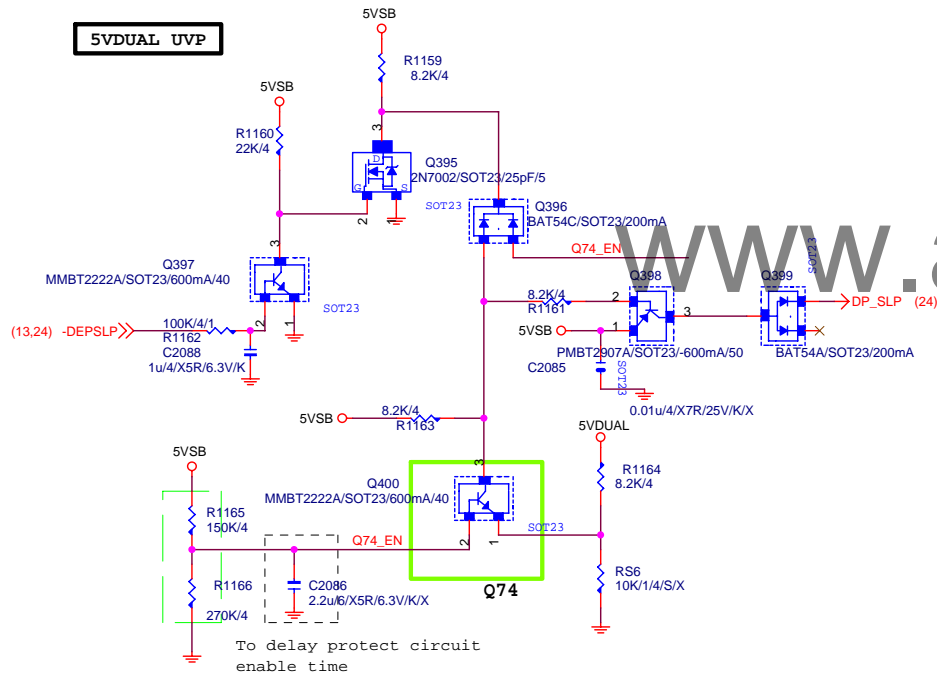
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sensor LPC DEBUG card
(always mount)



5VDUAL UVP



For CPU OC
200MHz
issue

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80 PORT, OV IC			
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